

Description

The DFI450HF12I4ME1 is a Half Bridge IGBT Power Module. It integrates high performance IGBT chips designed for the applications such as High Power supply and Motor control.



Features

- Blocking voltage:1200V
- Low saturation voltage $V_{CE(sat)}$
- Low Switching Losses
- 175°C maximum junction temperature
- Thermistor inside

Applications

- High Power Switching Applications
- Motor Drives
- Solar inverter Systems
- Wind Turbines

Circuit diagram

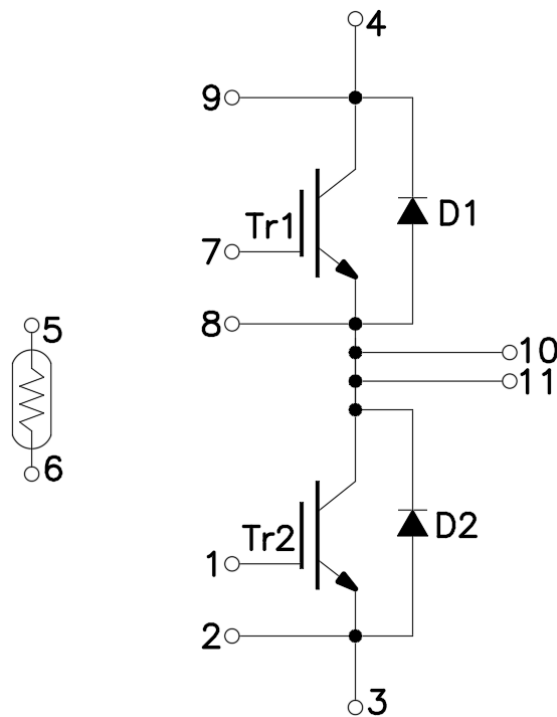


Figure 1. Out drawing & circuit diagram for DFI450HF12I4ME1

Pin Configuration and Marking Information

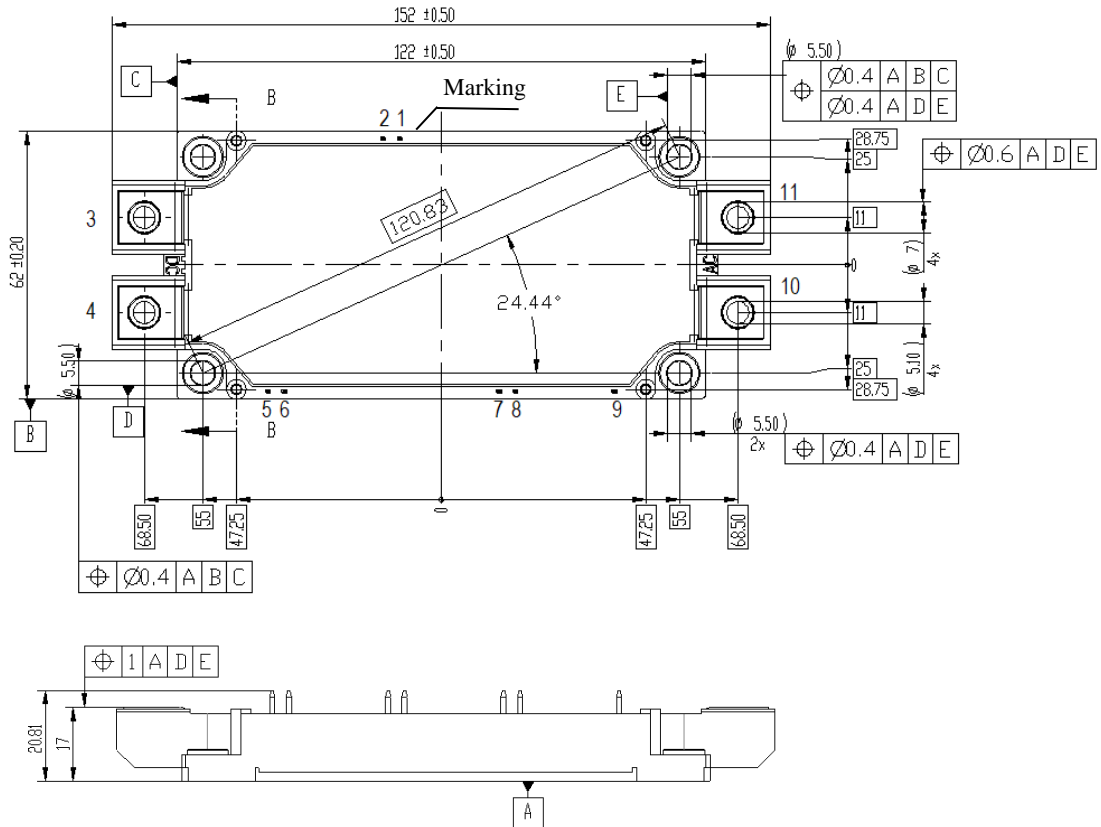


Figure 2. Pin configuration

Module

Parameter	Conditions	Value	Unit
Isolation Voltage	RMS, $f = 50\text{Hz}$, $t = 1\text{min}$	3.4	KV
Material of module baseplate	-	Cu	-
Creepage distance	terminal to heatsink terminal to terminal	14.5 13	mm
Clearance	terminal to heatsink terminal to terminal	12.5 10	mm
CTI	-	>200	-
Module lead resistance, terminals – chip	$T_c = 25^\circ\text{C}$	0.8	$\text{m}\Omega$
Mounting torque for module mounting	M5, M6	3 to 6	Nm
Weight	-	420	g

Maximum Ratings ($T_j=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Ratings	Unit
V_{CES}	Collector-Emitter Voltage	G-E Short	1200	V
V_{GES}	Gate-Emitter Voltage	C-E Short	± 20	V
I_C	DC Continuous Collector Current	$T_C=100^\circ\text{C}$	600	A
I_{CM}	Pulse Collector Current	$t_p=1\text{ms}$, Note1	1200	A
P_C	Maximum Power Dissipation	$T_C=25^\circ\text{C}$, $T_j=175^\circ\text{C}$ (IGBT)	3750	W
I_F	Diode forward Current	-	500	A
I_{FRM}	Repetitive peak forward Current	$t_p=1\text{ms}$, Note1	1000	A
I_{SC}	IGBT short circuit	$V_{GE} \leq 15\text{V}$, $V_{CC}=800\text{V}$, $t_p \leq 10\text{us}$, $V_{CE_{max}} = V_{CES} - L_{sCE} \cdot di/dt$ $T_j=150^\circ\text{C}$	2000	A
I^2t	I^2t -value	$V_R=0\text{V}$, $t_p=10\text{ms}$, $T_j=125^\circ\text{C}$ (Diode)	22500	A^2s
I^2t	I^2t -value	$V_R=0\text{V}$, $t_p=10\text{ms}$, $T_j=150^\circ\text{C}$ (Diode)	21100	A^2s
T_j	junction temperature	-	-40 to 175	$^\circ\text{C}$
T_{stg}	Storage temperature	-	-40 to 125	$^\circ\text{C}$

Note1: Pulse width limited by maximum junction temperature

NTC characteristics

Symbol	Parameter	Condition	Value			Unit
			Min.	Typ.	Max.	
R_{25}	Resistance	$T_C=25^\circ\text{C}$	-	5	-	$\text{k}\Omega$
$\Delta R/R$	Deviation of R_{100}	$T_C=100^\circ\text{C}$, $R_{100}=493\Omega$	-5	-	5	%
P_{25}	Power dissipation	$T_C=25^\circ\text{C}$	-	-	20	mW
$B_{25/50}$	B-value	$R_2 = R_{25} \exp [B_{25/50}(1/T_2 - 1/(298,15 \text{ K}))]$	-	3375	-	K
$B_{25/80}$	B-value	$R_2 = R_{25} \exp [B_{25/80}(1/T_2 - 1/(298,15 \text{ K}))]$	-	3411	-	K
$B_{25/100}$	B-value	$R_2 = R_{25} \exp [B_{25/100}(1/T_2 - 1/(298,15 \text{ K}))]$	-	3433	-	K

IGBT Electrical characteristics (T_j=25°C unless otherwise specified, chip)

Symbol	Item	Condition		Value			Unit
				Min.	Typ.	Max	
V _{CE(sat)} (Chip)	Collector-Emitter Saturation Voltage	I _C =450A V _{GE} =15V	T _j =25°C	-	1.65	1.95	V
			T _j =150°C	-	1.85	-	V
			T _j =175°C	-	1.90	-	V
V _{GE(th)}	Gate-Emitter threshold Voltage	I _C =19mA, V _{CE} =V _{GE}		5.0	-	6.8	V
Q _G	Gate charge	V _{GE} =-15V to +15V		-	3.3	-	uC
R _{Gint}	Internal gate resistor	-	T _j =25°C	-	1.5	-	Ω
C _{ies}	Input Capacitance	V _{CE} =25V, V _{GE} =0V f=1MHz	T _j =25°C	-	39	-	nF
C _{res}	Reverse transfer Capacitance			-	1.39	-	nF
I _{CES}	Collector- Emitter Cut off Current	V _{CE} =1200V, V _{GE} =0V	T _j =25°C	-	-	1	mA
I _{GES}	Gate-Emitter Leakage Current	V _{GE} =20V, V _{CE} =0V	T _j =25°C	-	-	1.13	uA
t _{d(on)}	Turn-on delay time	V _{CC} =600V I _C =450A V _{GE} =+15V/-8V R _G =1.0Ω Inductive load	T _j =25°C	-	180	-	ns
			T _j =125°C	-	191	-	
			T _j =175°C	-	195	-	
t _r	Rise time		T _j =25°C	-	52	-	ns
			T _j =125°C	-	54	-	
			T _j =175°C	-	58	-	
t _{d(off)}	Turn-off delay time		T _j =25°C	-	422	-	ns
			T _j =125°C	-	480	-	
			T _j =175°C	-	515	-	
t _f	Fall time		T _j =25°C	-	113	-	ns
		T _j =125°C	-	160	-		
		T _j =175°C	-	255	-		
E _{on}	Turn-on power dissipation	T _j =25°C	-	45.2	-	mJ	
		T _j =125°C	-	63.53	-		
		T _j =175°C	-	78.9	-		
E _{off}	Turn-off power dissipation	T _j =25°C	-	35.38	-	mJ	
		T _j =125°C	-	43.99	-		
		T _j =175°C	-	52.21	-		
R _{th(j-c)}	Thermal Resistance, Junction to Case (IGBT)		-	0.04	-	°C/W	
R _{th(c-s)}	Thermal Resistance, Case to sink (Conductive Grease applied)		-	0.02	-	°C/W	

Freewheeling Diode Electrical characteristics (T_j=25°C unless otherwise specified, chip)

Symbol	Item	Condition	Value			Unit	
			Min.	Typ.	Max		
V _F	Diode Forward Voltage	I _F = 450A, V _{GE} = 0V	T _j = 25°C	-	1.7	2.1	V
			T _j = 150°C	-	1.7	-	
			T _j = 175°C	-	1.65	-	
t _{rr}	Reverse recovery time	(Switch side) V _{CC} = 600V I _C = 450A	T _j = 25°C	-	0.34	-	us
			T _j = 125°C	-	0.598	-	
			T _j = 175°C	-	0.75	-	
I _{RM}	Peak reverse recovery Current	V _{GE} = +15V/-8V R _G = 1.0Ω (FRD side)	T _j = 25°C	-	283	-	A
			T _j = 125°C	-	262	-	
			T _j = 175°C	-	266	-	
Q _{rr}	Recovered charge	V _{rr} = 600V I _F = 450A V _{GE} = -8V	T _j = 25°C	-	33.41	-	uC
			T _j = 125°C	-	62.63	-	
			T _j = 175°C	-	84.87	-	
E _{rr}	Reverse recovered energy	Inductive load switching operation	T _j = 25°C	-	10.58	-	mJ
			T _j = 125°C	-	22.18	-	
			T _j = 175°C	-	29.89	-	
R _{th(j-c)}	Thermal Resistance, Junction to Case (Diode)		-	0.070	-	°C/W	
R _{th(c-s)}	Thermal Resistance, Case to sink (Conductive Grease applied)		-	0.022	-	°C/W	

Test Conditions

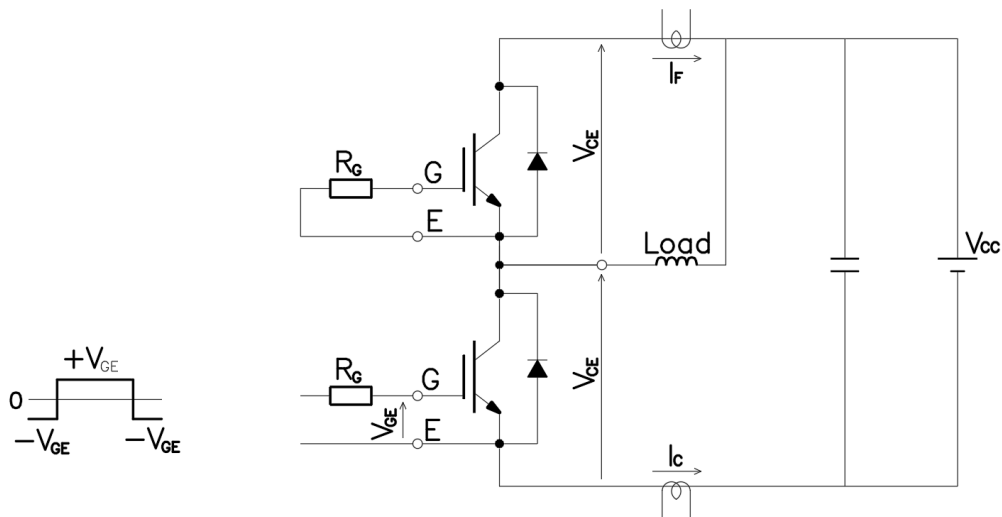


Figure 3. Switching time measure circuit

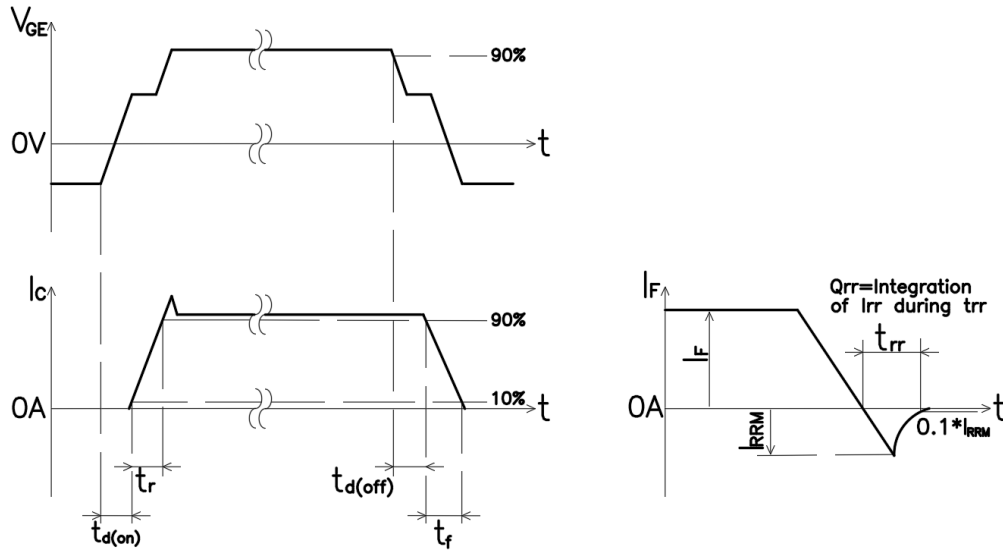


Figure 4. Switching time definition

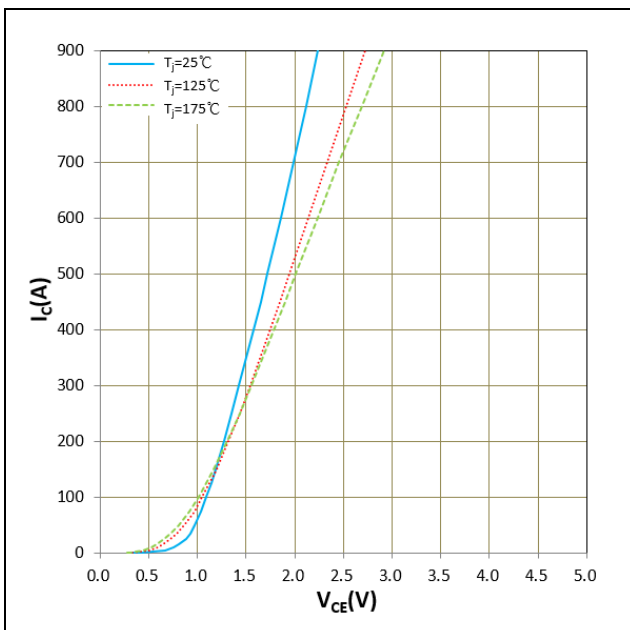


Figure 5. I_c vs V_{CE}
 $V_{GE} = 15V$

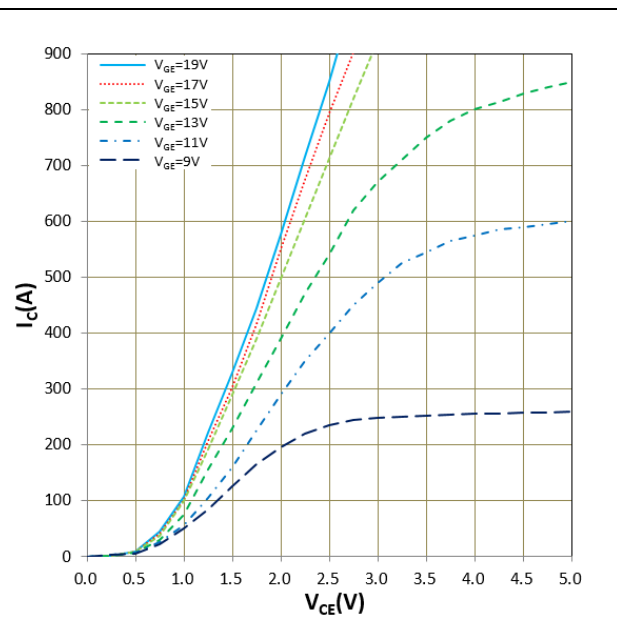


Figure 6. I_c vs V_{CE}
 $T_j = 175^\circ C$

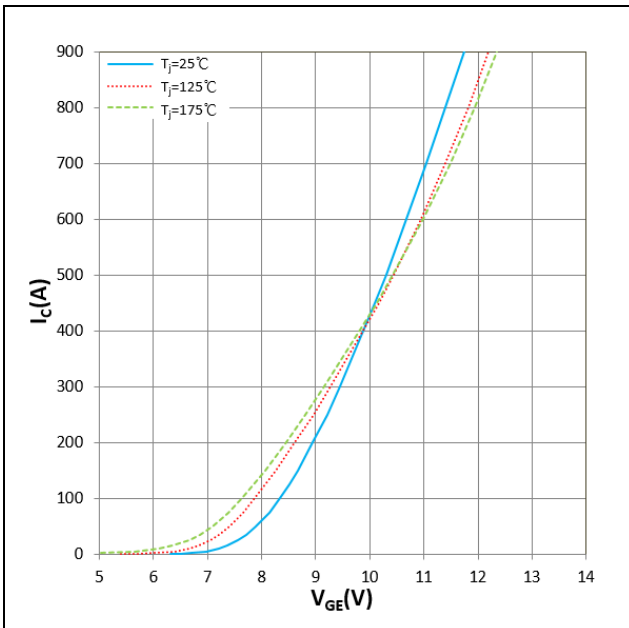


Figure 7. I_c vs V_{GE}
 $V_{CE}=20V$

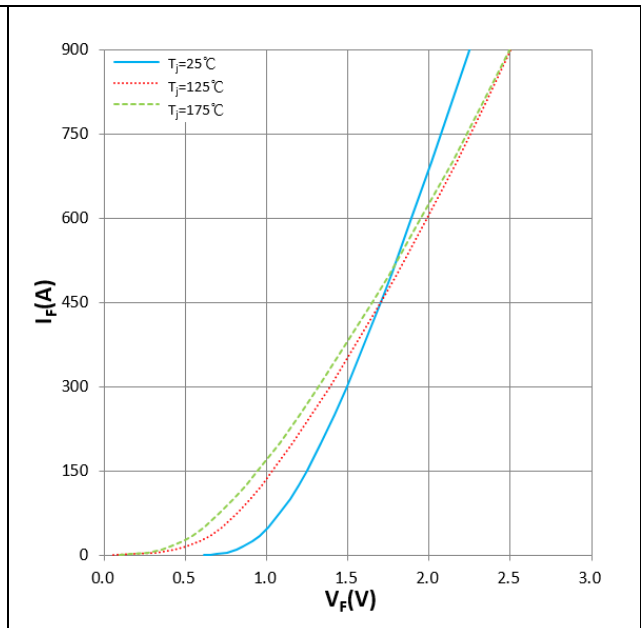


Figure 8. I_F vs V_F

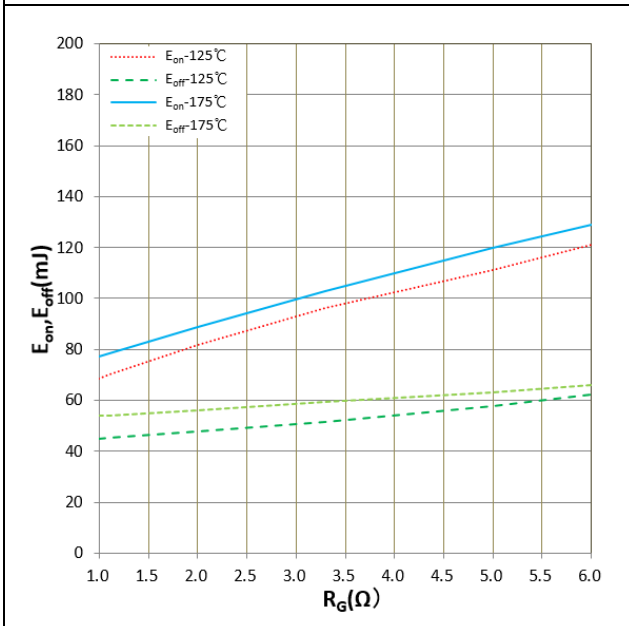


Figure 9. E_{on} , E_{off} vs R_G (Typ)
 $V_{CC}=600V$, $V_{GE}=+15V/-8V$, $I_c=450A$
Inductive Load

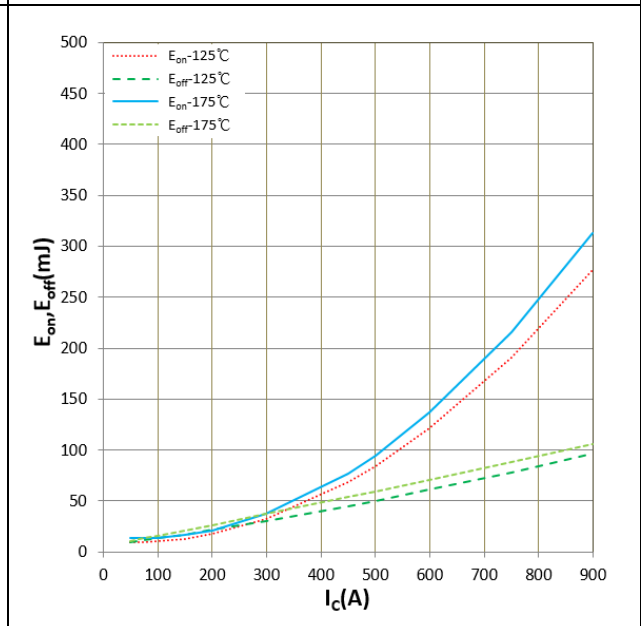


Figure 10. E_{on} , E_{off} vs I_c (Typ)
 $V_{CC}=600V$, $V_{GE}=+15V/-8V$, $R_G=1.0\Omega$
Inductive Load

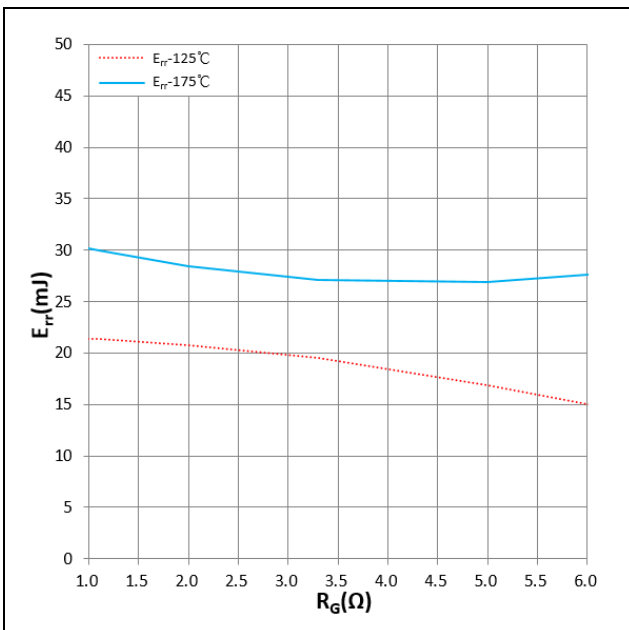


Figure 11. E_{rr} vs R_G (Typ)
 $V_{CC}=600V$, $V_{GE}=+15V/-8V$, $I_F=450A$
 Inductive Load

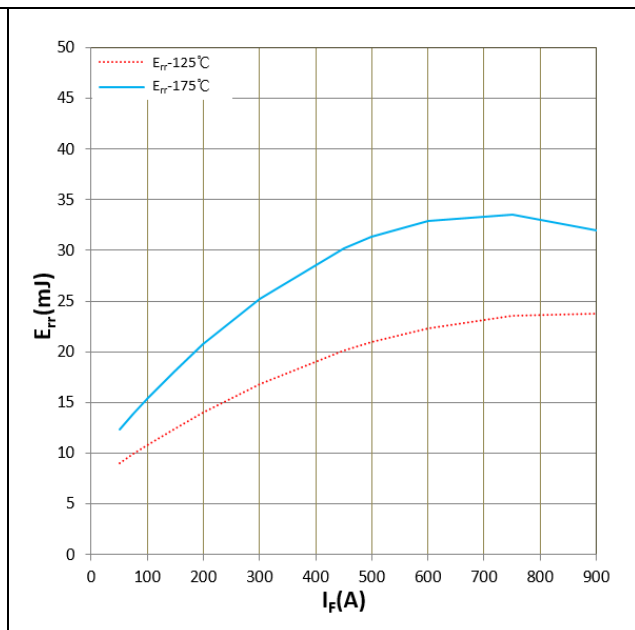


Figure 12. E_{rr} vs I_F (Typ)
 $V_{CC}=600V$, $V_{GE}=+15V/-8V$, $R_G=1.0\Omega$
 Inductive Load

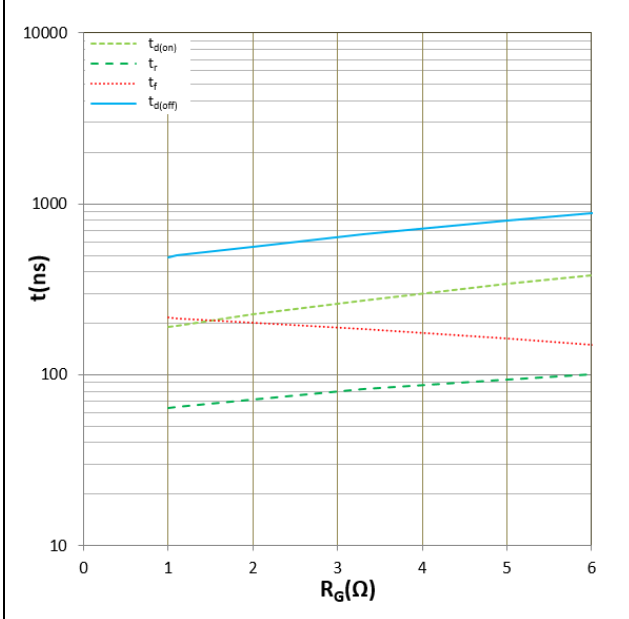


Figure 13. Switching time vs R_G (Typ)
 $V_{CC}=600V$, $V_{GE}=+15V/-8V$, $I_C=450A$
 $T_j=175^\circ C$, Inductive Load

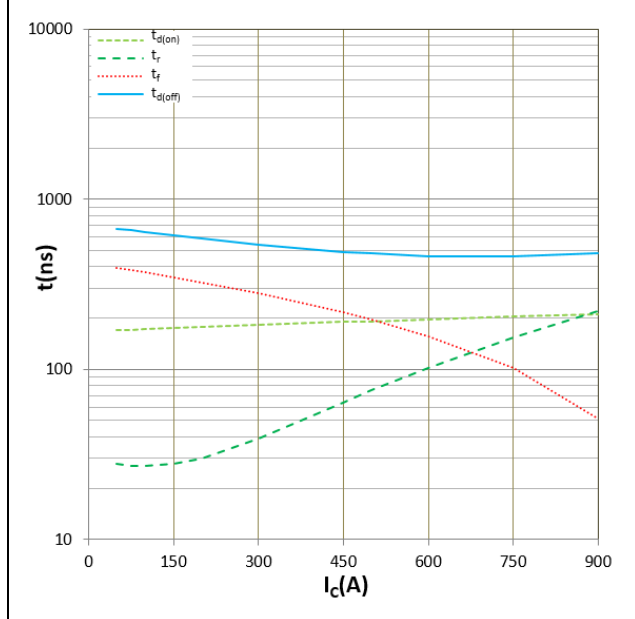


Figure 14. Switching time vs I_c (Typ)
 $V_{CC}=600V$, $V_{GE}=+15V/-8V$, $R_G=1.0\Omega$
 $T_j=175^\circ C$, Inductive Load

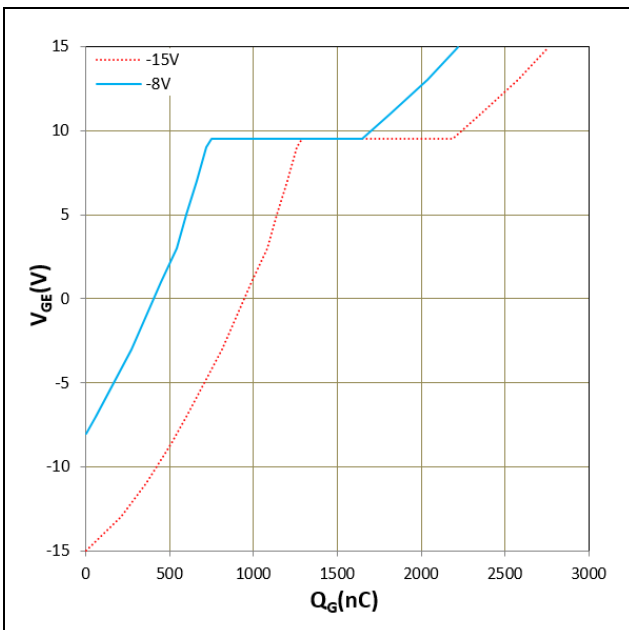


Figure 15. Gate charge

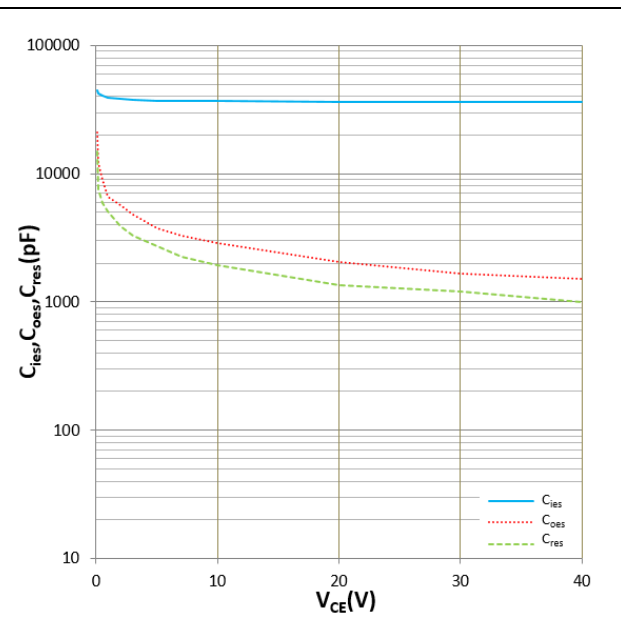


Figure 16. C_{ies} , C_{oes} , C_{res} vs V_{CE}
 $T_j = 25^\circ\text{C}$, $f = 100\text{KHz}$

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