

### Description

The DFH10AL12EZR1 is a 3-level Power Module. It integrates 1200V SiC MOSFET chips and 1200V IGBT chips designed for the applications such as Solar Inverter, High frequency switching, Energy storage Systems etc.



### Features

- Blocking voltage: 1200V
- $R_{ds(on)}$ : 9.5mΩ ( $V_{GS} = 15V$ )/8.3mΩ ( $V_{GS} = 18V$ )
- Low Switching Losses
- High current density
- Press FIT Contact Technology
- 175°C maximum junction temperature
- Thermistor inside

### Applications

- Solar inverter Systems
- Three-level applications
- Energy Storage Systems
- High Frequency Switching application

### Circuit diagram

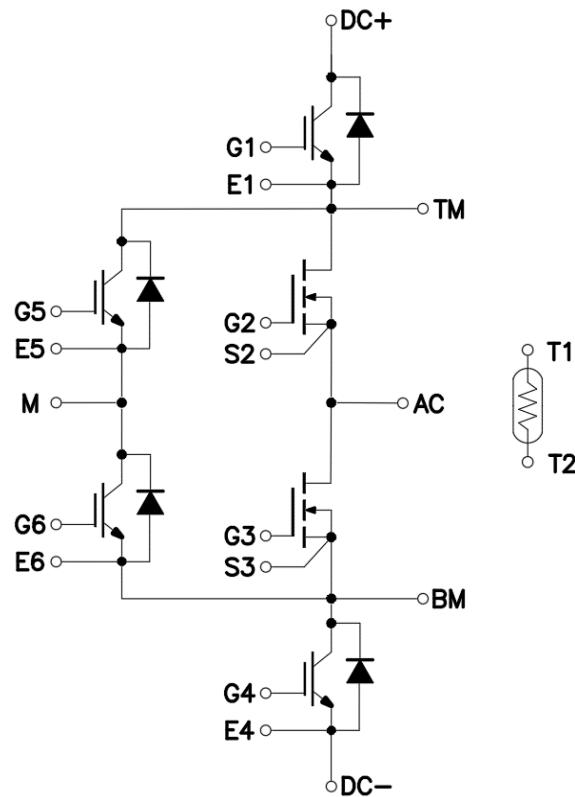


Figure 1. Out drawing & circuit diagram for DFH10AL12EZR1

## Pin Configuration

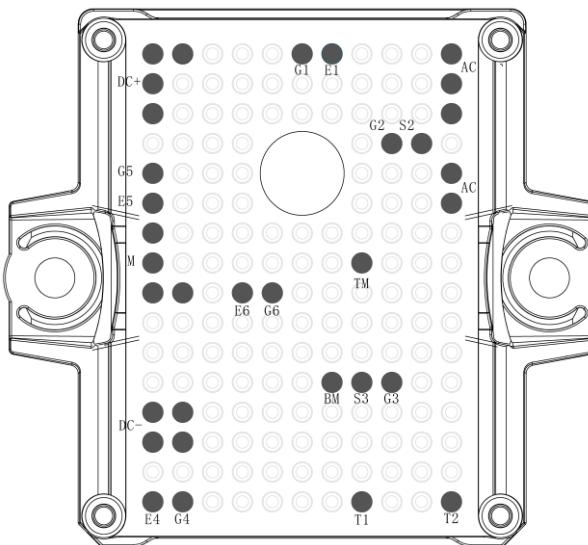


Figure 2. Pin configuration

## Module

Parameter	Conditions	Value	Unit
Isolation voltage	Main terminal to base plate, RMS, f =50Hz, t =1min	3.0	kV
Creepage distance	terminal to heatsink	11.5	mm
	terminal to terminal	6.3	
Clearance	terminal to heatsink	10.0	
	terminal to terminal	5.0	
Comparative tracking index	-	> 400	
Mounting torque for module mounting	Screw M4 baseplate to heatsink	1.8 to 2.2	Nm
Storage temperature	-	-40 to 125	°C
Weight	-	40	g

## NTC characteristics

Symbol	Parameter	Condition	Value			Unit
			Min.	Typ.	Max.	
R <sub>25</sub>	Resistance	T <sub>c</sub> =25°C	-	5	-	kΩ
ΔR/R	Deviation of R100	T <sub>c</sub> =100°C, R <sub>100</sub> =493Ω	-5	-	5	%
P <sub>25</sub>	Power dissipation	T <sub>c</sub> =25°C	-	-	20	mW
B <sub>25/50</sub>	B-value	R <sub>2</sub> =R <sub>25</sub> exp [B <sub>25/50</sub> (1/T <sub>2</sub> - 1/(298,15 K))]	-	3375	-	K
B <sub>25/80</sub>	B-value	R <sub>2</sub> =R <sub>25</sub> exp [B <sub>25/80</sub> (1/T <sub>2</sub> - 1/(298,15 K))]	-	3411	-	K
B <sub>25/100</sub>	B-value	R <sub>2</sub> =R <sub>25</sub> exp [B <sub>25/100</sub> (1/T <sub>2</sub> - 1/(298,15 K))]	-	3433	-	K

### Maximum Ratings (SiC MOSFET, $T_j=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Ratings	Unit
$V_{DSS}$	Drain-Source Voltage	G-S Short	1200	V
$V_{GSS}$	G-S Voltage	D-S Short, Note1	-10 to 22	V
$I_{DS}$	DC Continuous Drain Current	$T_C=125^\circ\text{C}$	100	A
$I_{SD}$	Source (Body diode) Current	$T_C=125^\circ\text{C}$	32	A
$I_{DP}$	Drain Pulse Current, Peak	Less than 1ms, Note2	200	A
$T_j$	junction temperature	-	-40 to 175	$^\circ\text{C}$

Note1: Recommended Operating Value, +18V/-5V, +15V/-4V, +15V/-5V

Note2: Pulse width limited by maximum junction temperature

### Maximum Ratings (IGBT and FRD, $T_j=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Ratings	Unit
$V_{CES}$	Collector-Emitter Voltage	G-E Short	1200	V
$V_{GES}$	Gate-Emitter Voltage	C-E Short	$\pm 20$	V
$I_{CN}$	DC Continuous Collector Current	$T_C=135^\circ\text{C}$	100	A
$I_{CM}$	Pulse Collector Current	$t_p=1\text{ms}$ , Note1	200	A
$I_F$	Diode forward Current	$T_C=100^\circ\text{C}$	100	A
$I_{FRM}$	Repetitive peak forward Current	$t_p=1\text{ms}$ , Note1	200	A
$T_j$	junction temperature	-	-40 to 175	$^\circ\text{C}$
$T_{stg}$	Storage temperature	-	-40 to 125	$^\circ\text{C}$

Note1: Pulse width limited by maximum junction temperature

### MOSFET Electrical characteristics ( $T_j=25^\circ\text{C}$ unless otherwise specified, chip)

Symbol	Item	Condition	Value			Unit	
			Min.	Typ.	Max		
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=200\mu\text{A}$	1200	-	-	V	
$I_{DSS}$	Zero gate voltage drain Current	$V_{DS}=1200\text{V}$ , $V_{GS}=0\text{V}$	-	2	-	$\mu\text{A}$	
$V_{GS(\text{th})}$	Gate-source threshold Voltage	$I_D=70\text{mA}$ , $V_{DS}=V_{GS}$ , $T_j=25^\circ\text{C}$	1.8	2.7	-	V	
		$I_D=70\text{mA}$ , $V_{DS}=V_{GS}$ , $T_j=175^\circ\text{C}$	-	2.05	-	V	
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=20\text{V}$ , $V_{DS}=0\text{V}$ , $T_j=25^\circ\text{C}$	-	200	-	nA	
$R_{DS(\text{on})}$ (Chip)	Static drain-source On-state resistance	$I_D=100\text{A}$ $V_{GS}=15\text{V}$	$T_j=25^\circ\text{C}$	-	9.5	$\text{m}\Omega$	
			$T_j=175^\circ\text{C}$	-	14.3	$\text{m}\Omega$	
		$I_D=100\text{A}$ $V_{GS}=-18\text{V}$	$T_j=25^\circ\text{C}$	-	8.3	$\text{m}\Omega$	
			$T_j=175^\circ\text{C}$	-	12.6	$\text{m}\Omega$	
$V_{DS(\text{on})}$ (Chip)	Static drain-source On-state Voltage	$I_D=100\text{A}$ $V_{GS}=15\text{V}$	$T_j=25^\circ\text{C}$	-	0.95	V	
			$T_j=175^\circ\text{C}$	-	1.43	V	
		$I_D=100\text{A}$ $V_{GS}=-18\text{V}$	$T_j=25^\circ\text{C}$	-	0.83	1.25	V
			$T_j=175^\circ\text{C}$	-	1.26	-	V
$C_{iss}$	Input Capacitance	$V_D=800\text{V}$ , $V_{GS}=0\text{V}$ $f=1\text{MHz}$ , $V_{AC}=25\text{mV}$	-	11.6	-	nF	
$C_{oss}$	Output Capacitance		-	0.352	-	nF	
$C_{rss}$	Reverse transfer Capacitance		-	0.028	-	nF	

Q <sub>G</sub>	Total gate charge	V <sub>DD</sub> =800V, I <sub>D</sub> =120A, V <sub>GS</sub> =-5/+15V	-	360	-	nC
R <sub>Gint</sub>	Internal Gate Resistance	f=1Mhz, V <sub>AC</sub> =25mV	-	0.65	-	Ω
t <sub>d(on)</sub>	Turn-on delay time	V <sub>DD</sub> =600V I <sub>D</sub> =100A V <sub>GS</sub> =+15V/-4V R <sub>g</sub> =5.1Ω Inductive load switching operation	T <sub>j</sub> =25°C	-	43	-
			T <sub>j</sub> =150°C	-	40	-
t <sub>r</sub>	Rise time	V <sub>DD</sub> =600V I <sub>D</sub> =100A V <sub>GS</sub> =+15V/-4V R <sub>g</sub> =5.1Ω Inductive load switching operation	T <sub>j</sub> =25°C	-	23	-
			T <sub>j</sub> =150°C	-	19	-
t <sub>d(off)</sub>	Turn-off delay time	V <sub>DD</sub> =600V I <sub>D</sub> =100A V <sub>GS</sub> =+15V/-4V R <sub>g</sub> =5.1Ω Inductive load switching operation	T <sub>j</sub> =25°C	-	112	-
			T <sub>j</sub> =150°C	-	120	-
t <sub>f</sub>	Fall time	V <sub>DD</sub> =600V I <sub>D</sub> =100A V <sub>GS</sub> =+15V/-4V R <sub>g</sub> =5.1Ω Inductive load switching operation	T <sub>j</sub> =25°C	-	15	-
			T <sub>j</sub> =150°C	-	40	-
E <sub>on</sub>	Turn-on power dissipation	V <sub>DD</sub> =600V I <sub>D</sub> =100A V <sub>GS</sub> =+15V/-4V R <sub>g</sub> =5.1Ω Inductive load switching operation	T <sub>j</sub> =25°C	-	2.22	-
			T <sub>j</sub> =150°C	-	2.31	-
E <sub>off</sub>	Turn-off power dissipation	V <sub>DD</sub> =600V I <sub>D</sub> =100A V <sub>GS</sub> =+15V/-4V R <sub>g</sub> =5.1Ω Inductive load switching operation	T <sub>j</sub> =25°C	-	1.50	-
			T <sub>j</sub> =150°C	-	1.59	-
R <sub>th(j-c)</sub>	FET Thermal Resistance	Junction to Case/MOSFET	-	0.32	-	K/W
R <sub>th(c-s)</sub>	Thermal Resistance, Case to sink (Conductive Grease applied) , Note1		-	0.12	-	K/W

Note1: Assumes Thermal Conductivity of grease is 2.8W/m·K and thickness is 50um.

### Body Diode Electrical characteristics (T<sub>j</sub>=25°C unless otherwise specified, chip: Target)

Symbol	Item	Condition		Value			Unit
				Min.	Typ.	Max	
V <sub>SD</sub>	Body Diode Forward Voltage	V <sub>GS</sub> =-5V I <sub>SD</sub> =100A	T <sub>j</sub> =25°C	-	5.1	-	V
			T <sub>j</sub> =175°C	-	4.6	-	
T <sub>rr</sub>	Reverse recovery time	V <sub>DD</sub> =600V I <sub>D</sub> =100A V <sub>GS</sub> =+15V/-4V R <sub>g</sub> =5.1Ω Inductive load switching operation	T <sub>j</sub> =25°C	-	26	-	ns
			T <sub>j</sub> =150°C	-	50	-	
Q <sub>rr</sub>	Reverse recovery charge	V <sub>DD</sub> =600V I <sub>D</sub> =100A V <sub>GS</sub> =+15V/-4V R <sub>g</sub> =5.1Ω Inductive load switching operation	T <sub>j</sub> =25°C	-	0.75	-	μC
			T <sub>j</sub> =150°C	-	3.20	-	
E <sub>rr</sub>	Diode switching power dissipation	V <sub>DD</sub> =600V I <sub>D</sub> =100A V <sub>GS</sub> =+15V/-4V R <sub>g</sub> =5.1Ω Inductive load switching operation	T <sub>j</sub> =25°C	-	0.12	-	mJ
			T <sub>j</sub> =150°C	-	0.79	-	

### IGBT Electrical characteristics (T<sub>j</sub>=25°C unless otherwise specified, chip)

Symbol	Item	Condition		Value			Unit
				Min.	Typ.	Max	
V <sub>CESat</sub> (Chip)	Collector-Emitter Saturation Voltage	I <sub>C</sub> =100A V <sub>GE</sub> =15V	T <sub>j</sub> =25°C	-	1.56	-	V
			T <sub>j</sub> =175°C	-	1.81	-	
V <sub>GE(th)</sub>	Gate-Emitter threshold Voltage	I <sub>C</sub> =2.6mA, V <sub>CES</sub> =10V		-	5.9	-	V
Q <sub>G</sub>	Gate charge	V <sub>GE</sub> =-15V to +15V		-	2.0	-	uC
R <sub>Gint</sub>	Internal gate resistor	-	T <sub>j</sub> =25°C	-	13	-	Ω
C <sub>ies</sub>	Input Capacitance	V <sub>CES</sub> =25V, V <sub>GE</sub> =0V f=1MHz	T <sub>j</sub> =25°C	-	3.56	-	nF
C <sub>res</sub>	Reverse transfer Capacitance		T <sub>j</sub> =25°C	-	0.04	-	nF
I <sub>CES</sub>	Collector- Emitter Cut off Current	V <sub>CES</sub> =1200V, V <sub>GE</sub> =0V	T <sub>j</sub> =25°C	-	-	0.01	mA
I <sub>GES</sub>	Gate-Emitter Leakage Current	V <sub>GE</sub> =20V, V <sub>CES</sub> =0V	T <sub>j</sub> =25°C	-	-	0.1	uA

t <sub>d(on)</sub>	Turn-on delay time	V <sub>CC</sub> =600V I <sub>C</sub> = 100A V <sub>GE</sub> = +15V/-15V R <sub>g</sub> = 1.5Ω Inductive load	T <sub>j</sub> =25°C	-	100	-	ns	
			T <sub>j</sub> =125°C	-	108	-		
			T <sub>j</sub> =175°C	-	114	-		
t <sub>r</sub>	Rise time		T <sub>j</sub> =25°C	-	31	-	ns	
			T <sub>j</sub> =125°C	-	34	-		
			T <sub>j</sub> =175°C	-	37	-		
t <sub>d(off)</sub>	Turn-off delay time		T <sub>j</sub> =25°C	-	228	-	ns	
			T <sub>j</sub> =125°C	-	299	-		
			T <sub>j</sub> =175°C	-	342	-		
t <sub>f</sub>	Fall time		T <sub>j</sub> =25°C	-	210	-	ns	
			T <sub>j</sub> =125°C	-	308	-		
			T <sub>j</sub> =175°C	-	384	-		
E <sub>on</sub>	Turn-on power dissipation		T <sub>j</sub> =25°C	-	1.24	-	mJ	
			T <sub>j</sub> =125°C	-	1.80	-		
			T <sub>j</sub> =175°C	-	2.01	-		
E <sub>off</sub>	Turn-off power dissipation		T <sub>j</sub> =25°C	-	6.7	-	mJ	
			T <sub>j</sub> =125°C	-	12.2	-		
			T <sub>j</sub> =175°C	-	15.1	-		
R <sub>th(j-c)</sub>	Thermal Resistance, Junction to Case (IGBT)			-	0.16	-	K/W	
R <sub>th(c-s)</sub>	Thermal Resistance, Case to sink (Conductive Grease applied) , Note1			-	0.12	-	K/W	

Note1: Assumes Thermal Conductivity of grease is 2.8W/m·K and thickness is 50um.

### Freewheeling Diode Electrical characteristics (T<sub>j</sub>=25°C unless otherwise specified, chip)

Symbol	Item	Condition	Value			Unit		
			Min.	Typ.	Max			
V <sub>F</sub>	Diode Forward Voltage	I <sub>F</sub> = 100A, V <sub>GE</sub> = 0V	T <sub>j</sub> =25°C	-	1.9	-	V	
			T <sub>j</sub> =175°C	-	1.75	-		
t <sub>rr</sub>	Reverse recovery time	(Switch side) V <sub>CC</sub> =600V I <sub>C</sub> =100A V <sub>GE</sub> = +15V/-15V R <sub>g</sub> = 1.5Ω (FRD side) V <sub>rr</sub> =600V I <sub>F</sub> =100A V <sub>GE</sub> = +15V/-15V Inductive load switching operation	T <sub>j</sub> =25°C	-	0.36	-	us	
			T <sub>j</sub> =125°C	-	0.53	-		
			T <sub>j</sub> =175°C	-	0.71	-		
I <sub>RM</sub>	Peak reverse recovery Current		T <sub>j</sub> =25°C	-	103	-	A	
			T <sub>j</sub> =125°C	-	131	-		
			T <sub>j</sub> =175°C	-	154	-		
Q <sub>rr</sub>	Recovered charge		T <sub>j</sub> =25°C	-	9.32	-	uC	
			T <sub>j</sub> =125°C	-	16.7	-		
			T <sub>j</sub> =175°C	-	27.2	-		
E <sub>rr</sub>	Reverse recovered energy		T <sub>j</sub> =25°C	-	3.74	-	mJ	
			T <sub>j</sub> =125°C	-	6.90	-		
			T <sub>j</sub> =175°C	-	8.77	-		
R <sub>th(j-c)</sub>	Thermal Resistance, Junction to Case (Diode)			-	0.28	-	K/W	
R <sub>th(c-s)</sub>	Thermal Resistance, Case to sink (Conductive Grease applied) , Note1			-	0.12	-	K/W	

Note1: Assumes Thermal Conductivity of grease is 2.8W/m·K and thickness is 50um.

## Test Conditions

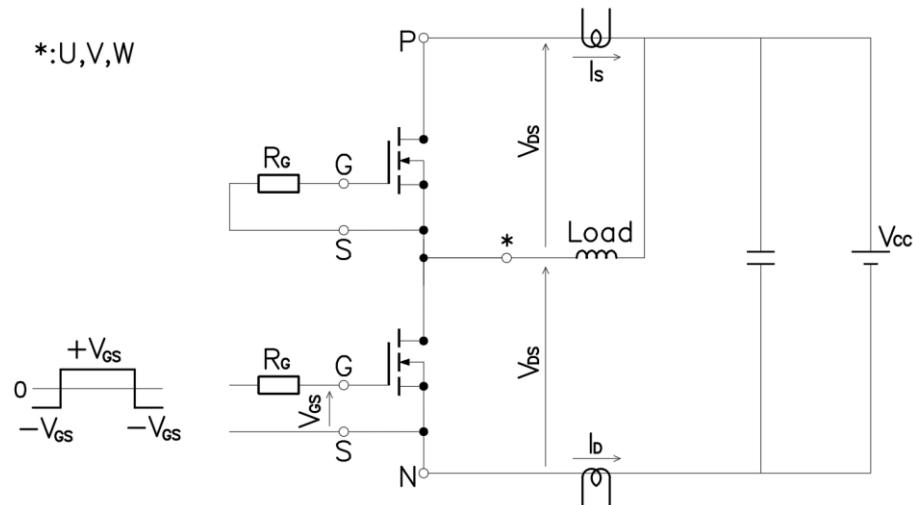


Figure 3. Switching time measure circuit

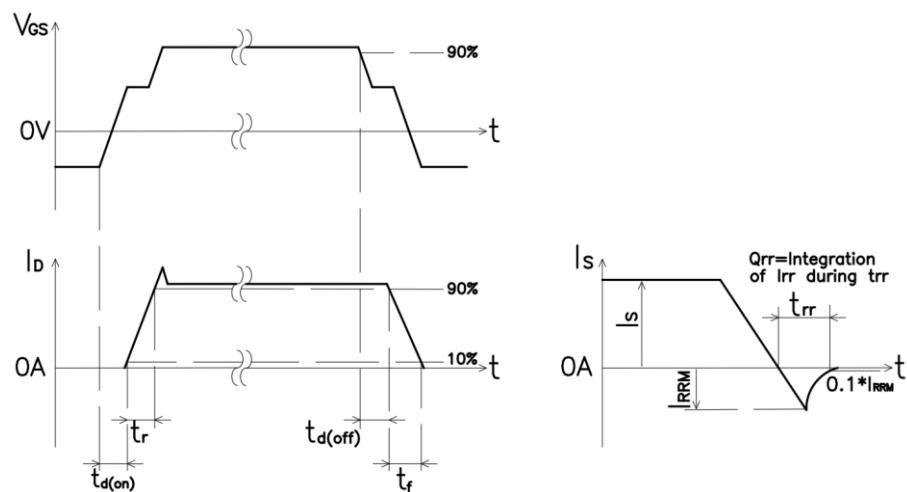
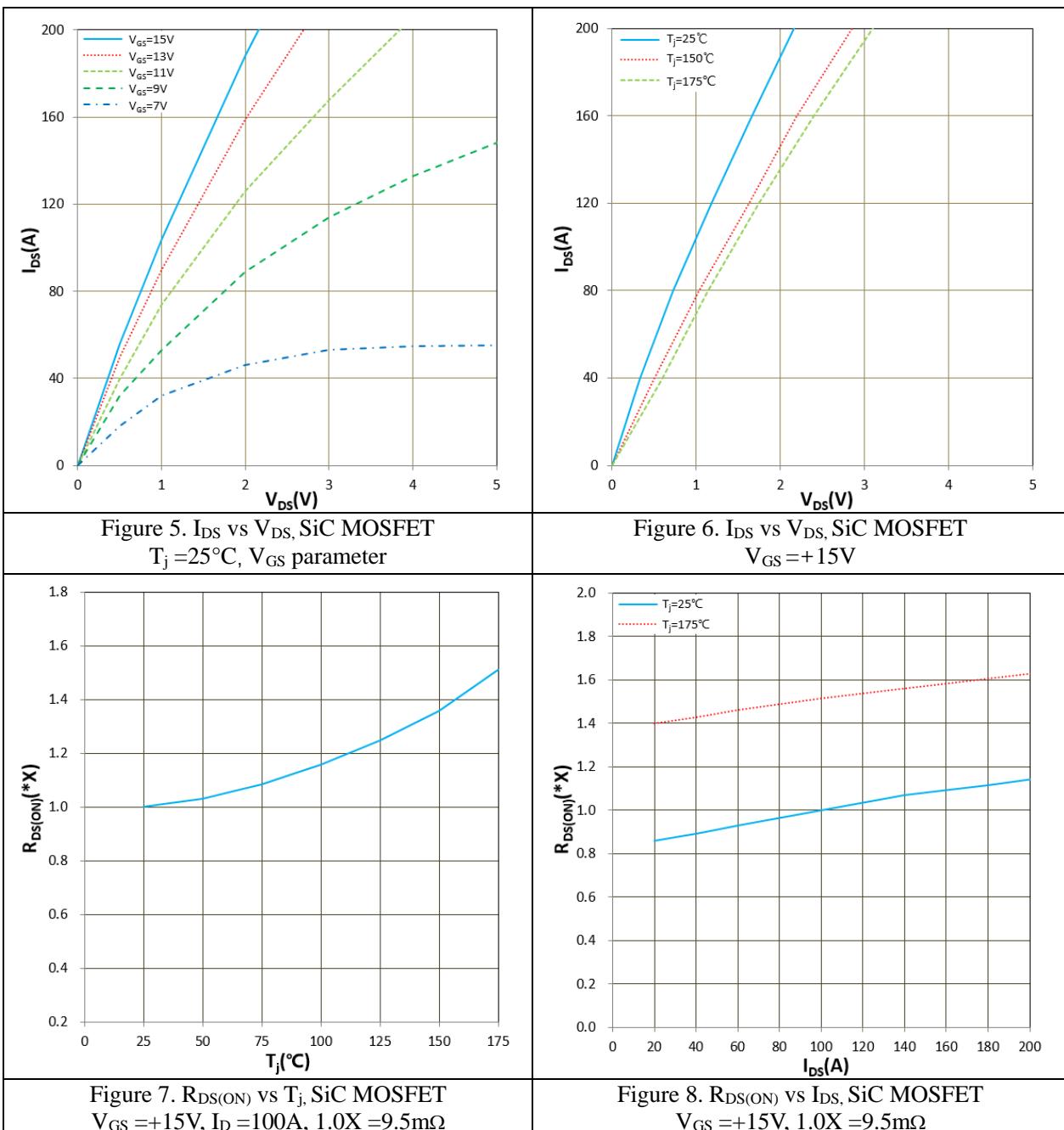
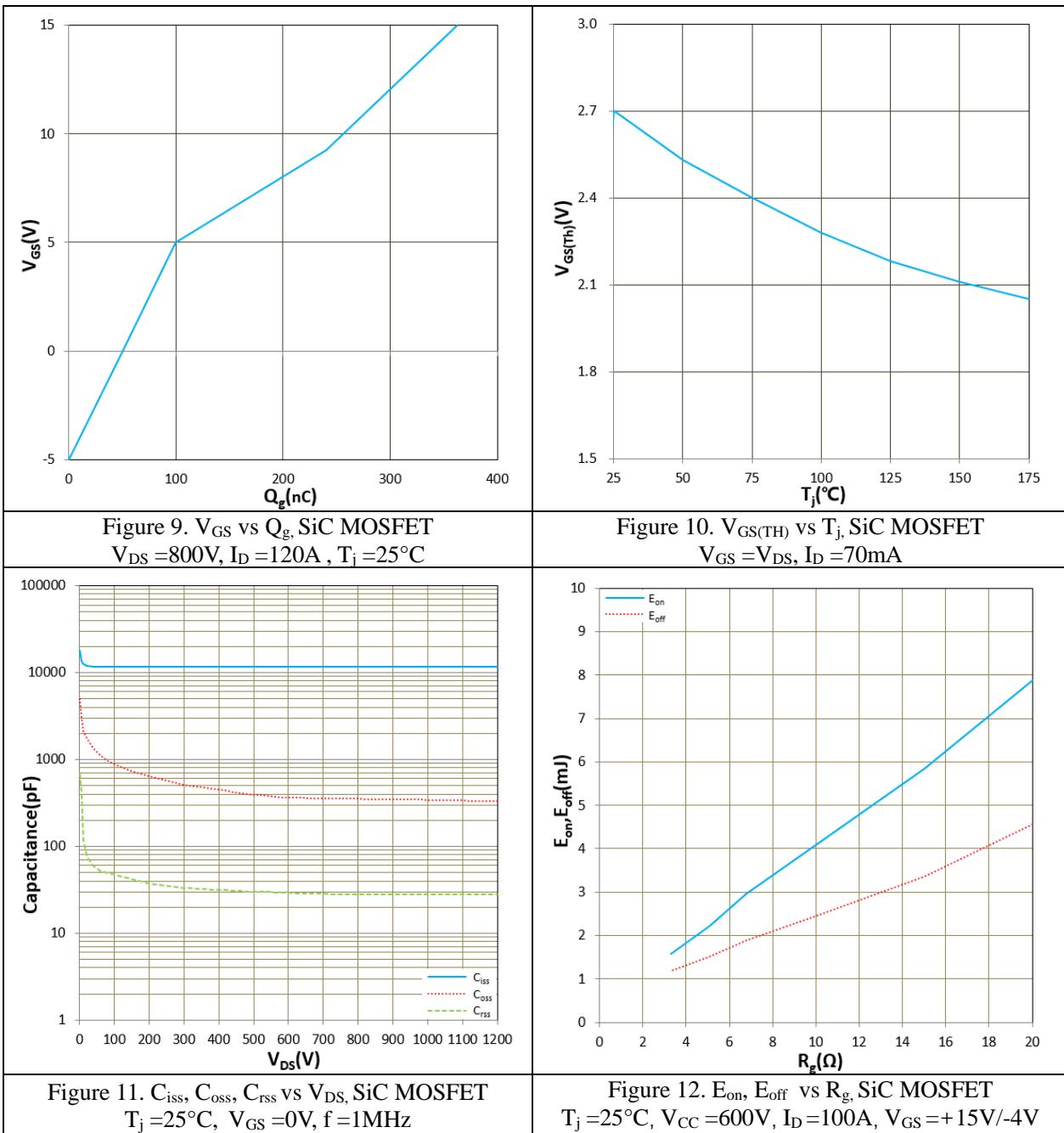
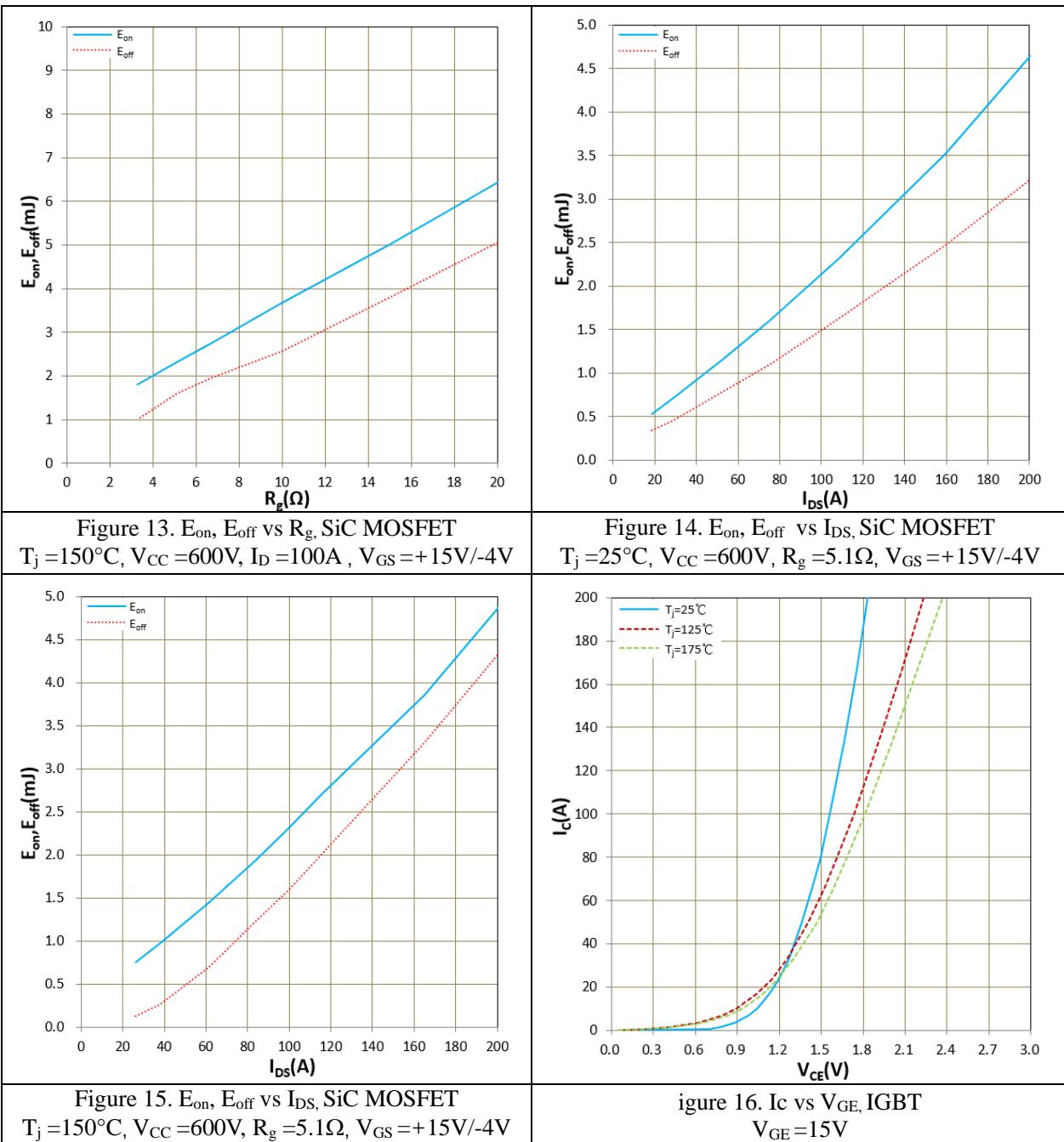
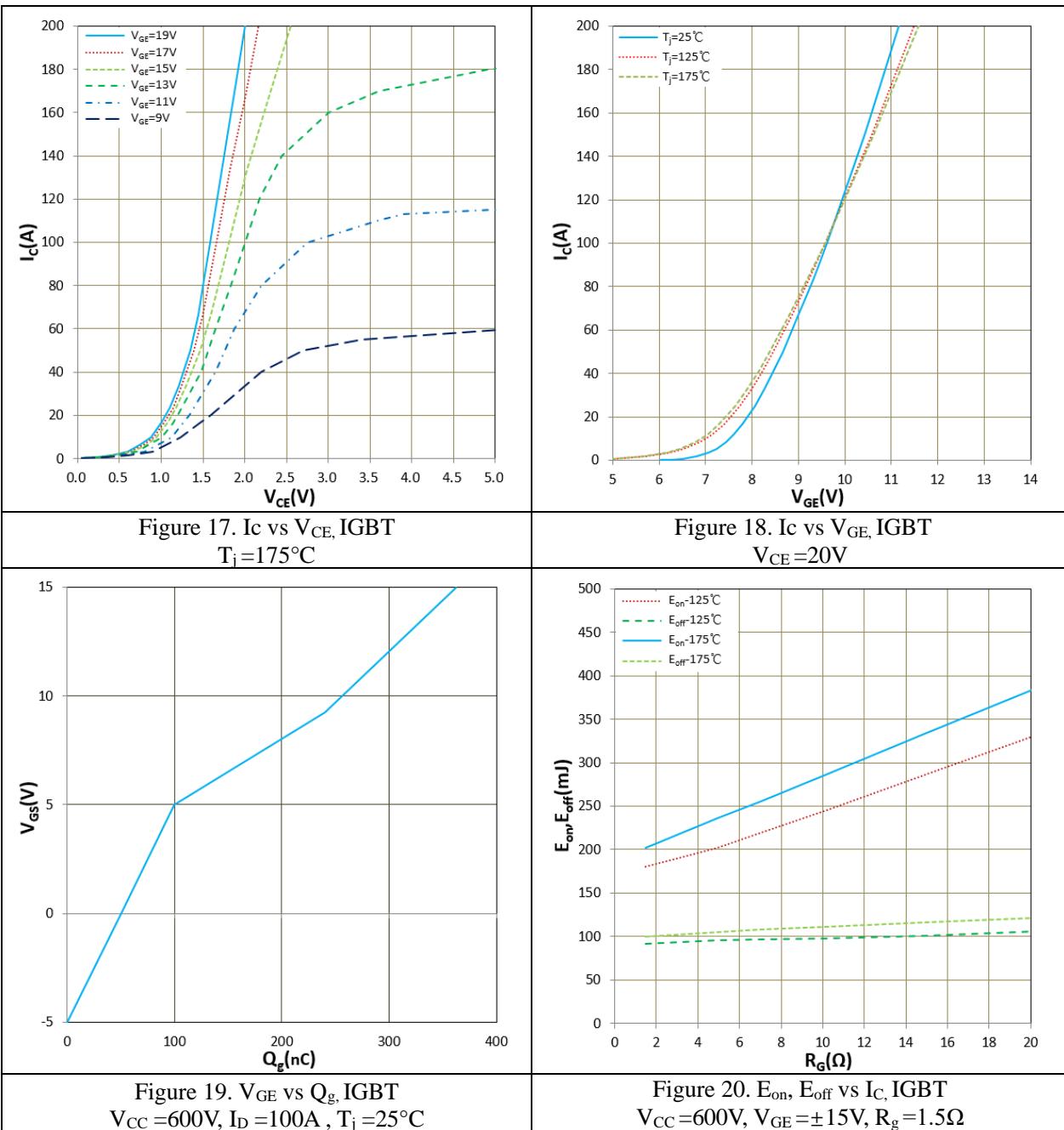


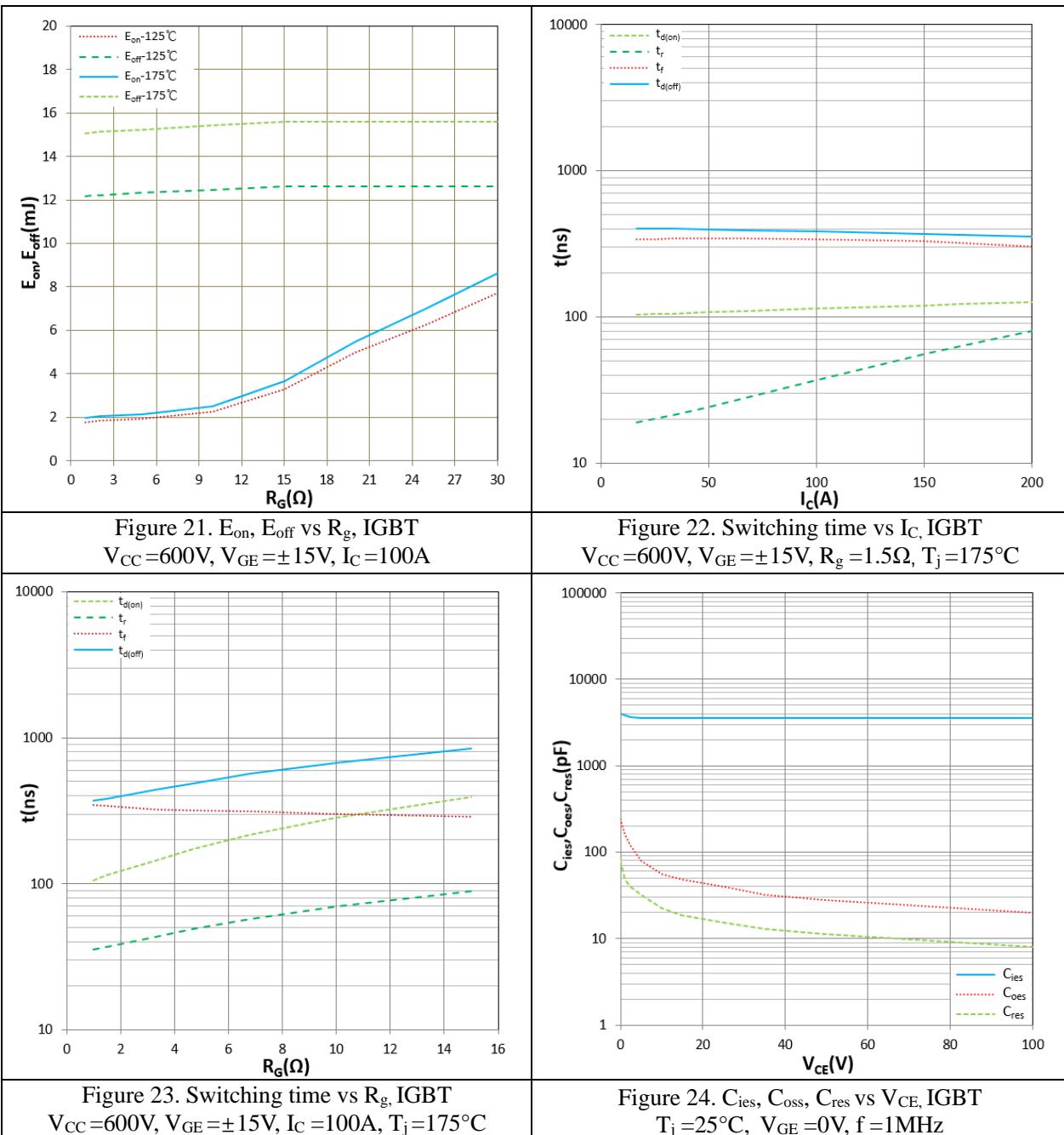
Figure 4. Switching time definition

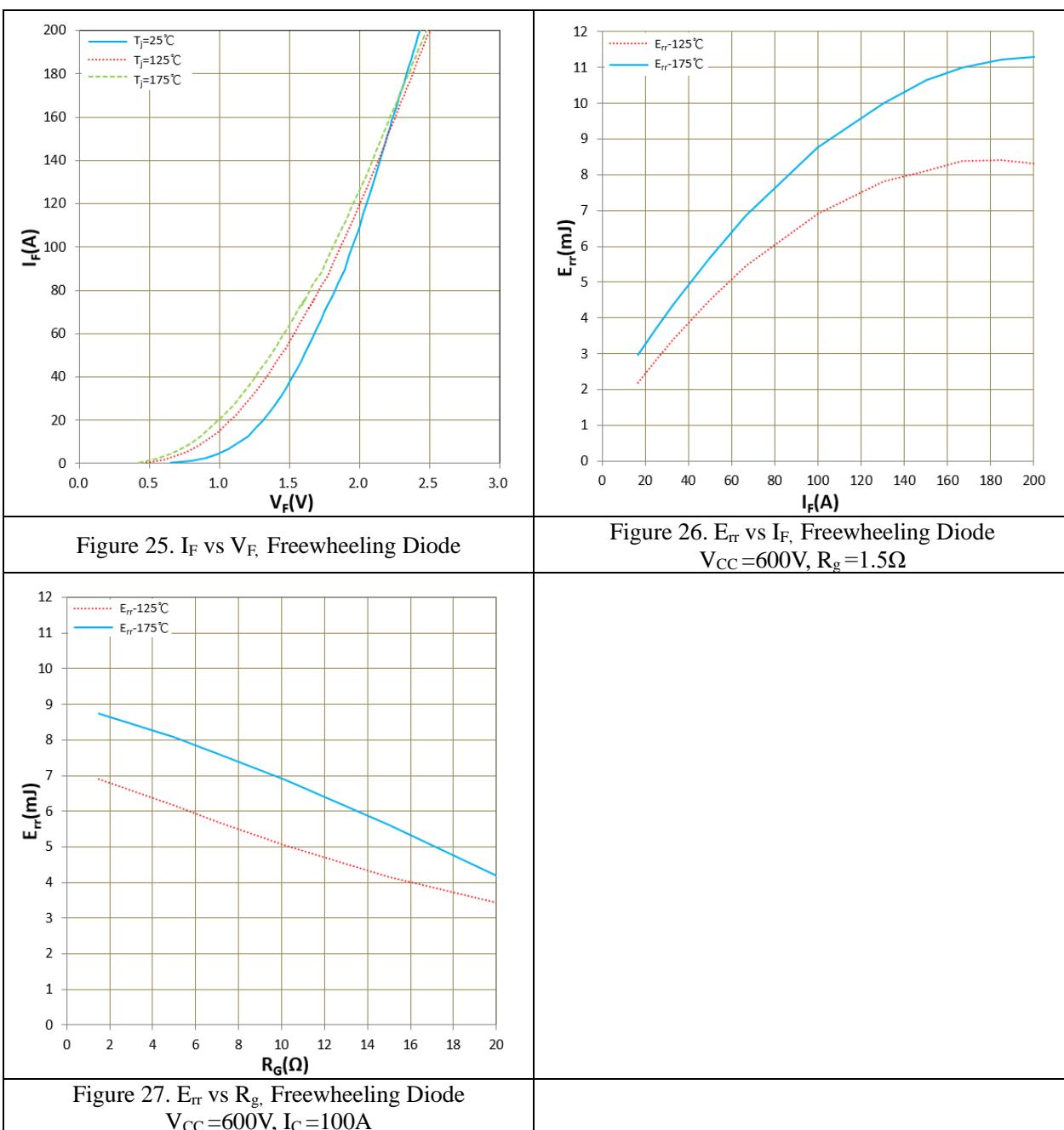




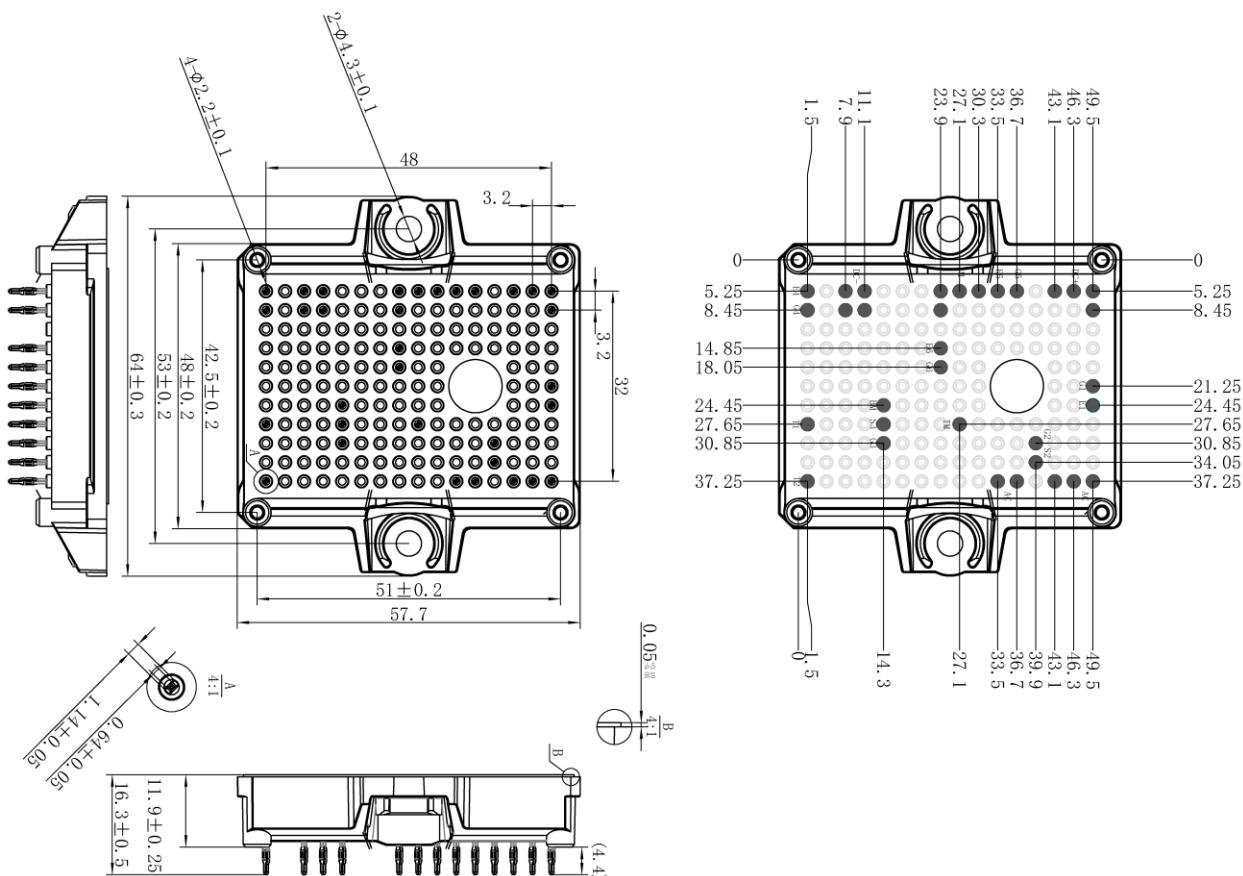








## Package dimensions



## IMPORTANT NOTICE

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