

Description

The DFS04HF12EZC1 is a Half Bridge SiC MOSFET half bridge Power Module. It integrates high performance SiC MOSFET chips designed for the applications such as Solar Inverter, UPS, Fuel cell- DC/DC converter, Energy storage Systems.



Features

- Blocking voltage:1200V
- $R_{ds(on)}$:4.7mΩ ($V_{GS}=15V$)/ 4.2mΩ ($V_{GS}=18V$)
- 200A@ $T_f=120^{\circ}C$
- Low Switching Losses
- 175°C maximum junction temperature
- Si₃N₄ AMB
- Thermistor inside

Applications

- Solar inverter Systems
- Fuel cell-DC/DC converter
- Uninterruptible Power Supplier
- Energy Storage Systems

Circuit diagram

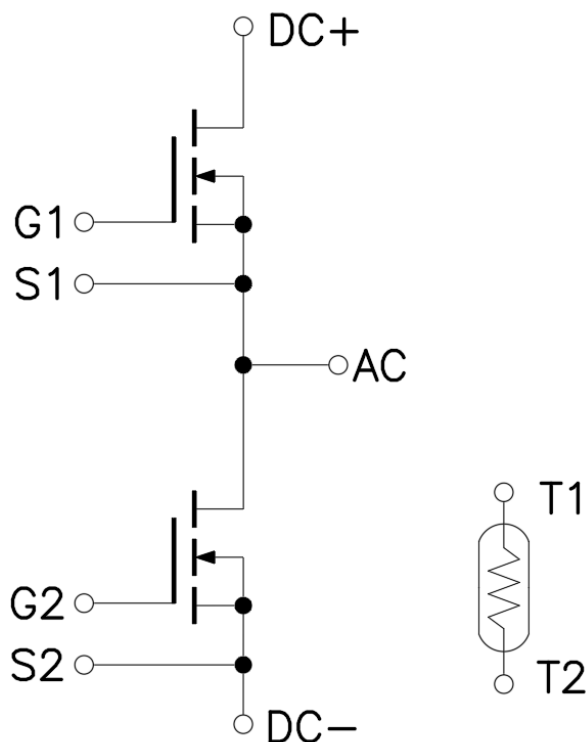


Figure 1. Out drawing & circuit diagram for DFS04HF12EZC1

Pin Configuration and Function Description

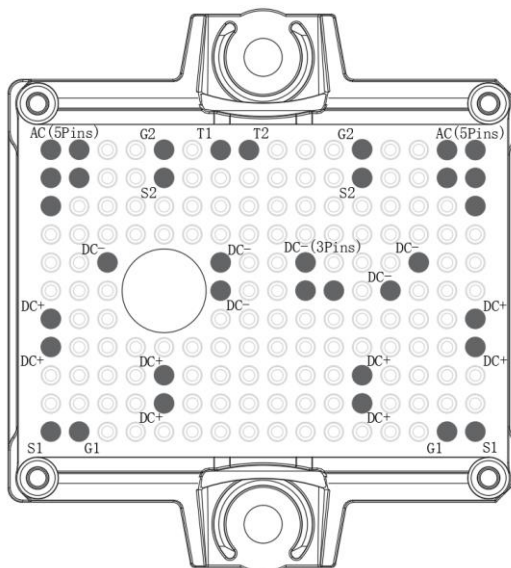


Figure 2. Pin configuration

PIN No.	Symbol	Description
1-5(5pins)	AC	Output terminal of half bridge
6	S2	Low side source signal terminal
7	G2	Low side gate signal terminal
8	T1	Thermistor connection 1
9	T2	Thermistor connection 2
10	S2	Low side source signal terminal
11	G2	Low side gate signal terminal
12-16(5pins)	AC	Output terminal of half bridge
17-24(8pins)	DC –	DC – Bus connection
25-32(8pins)	DC +	DC + Bus connection
33	S1	High side source signal terminal
34	G1	High side gate signal terminal
35	G1	High side gate signal terminal
36	S1	High side source signal terminal

Module

Parameter	Conditions	Value	Unit
Isolation voltage	RMS, f =50Hz, t =1min	3.4	kV
Clearance	Terminal to Terminal	5	mm
	Terminal to Heatsink	10	mm
Creepage distance	Terminal to Terminal	6.3	mm
	Terminal to Heatsink	12.7	mm
Comparative Tracking Index	-	400	-

Maximum Ratings (T_j = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Ratings	Unit
V _{DSS}	Drain-Source Voltage	G-S Short	1200	V
V _{GSS}	G-S Voltage	D-S Short, Note1	-10 to 22	V
I _{DS}	DC Continuous Drain Current	T _f = 120°C	200	A
I _{SD}	Source (Body diode) Current	T _f = 120°C, with ON signal	200	A
I _{DP}	Drain Pulse Current, Peak	Less than 1ms, Note2	400	A
T _j	junction temperature	-	-40 to 175	°C
T _{stg}	Storage temperature	-	-40 to 125	°C

Note1: Recommended Operating Value, +18V/-5V, +15/-5V, +15/-4V

Note2: Pulse width limited by maximum junction temperature

NTC characteristics

Symbol	Parameter	Condition	Value			Unit
			Min.	Typ.	Max.	
R ₂₅	Resistance	T _C = 25°C	-	5	-	kΩ
ΔR/R	Deviation of R ₁₀₀	T _C = 100°C, R ₁₀₀ = 493Ω	5	-	5	%
P ₂₅	Power dissipation	T _C = 25°C	-	-	20	mW
B _{25/50}	B-value	R ₂ = R ₂₅ exp [B _{25/50} (1/T ₂ - 1/(298,15 K))]	-	3375	-	K
B _{25/80}	B-value	R ₂ = R ₂₅ exp [B _{25/80} (1/T ₂ - 1/(298,15 K))]	-	3411	-	K
B _{25/100}	B-value	R ₂ = R ₂₅ exp [B _{25/100} (1/T ₂ - 1/(298,15 K))]	-	3433	-	K

MOSFET Electrical characteristics (T_j=25°C unless otherwise specified, chip)

Symbol	Item	Condition		Value			Unit
				Min.	Typ.	Max	
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =400uA		1200	-	-	V
I _{DSS}	Zero gate voltage drain Current	V _{DS} =1200V, V _{GS} =0V		-	4	-	μA
V _{GS(th)}	Gate-source threshold Voltage	I _D =140mA, V _{DS} =V _{GS} , T _j =25°C		1.8	2.7	-	V
		I _D =140mA, V _{DS} =V _{GS} , T _j =175°C		-	2.05	-	V
I _{GSS}	Gate-Source Leakage Current	V _{GS} =20V, V _{DS} =0V, T _j =25°C		-	400	-	nA
R _{DS(on)} (Chip)	Static drain-source On-state resistance	I _D =200A V _{GS} =+15V	T _j =25°C	-	4.7	-	mΩ
			T _j =175°C	-	7.2	-	mΩ
		I _D =200A V _{GS} =+18V	T _j =25°C	-	4.2	6.3	mΩ
			T _j =175°C	-	6.3	-	mΩ
V _{DS(on)} (Chip)	Static drain-source On-state Voltage	I _D =200A V _{GS} =+15V	T _j =25°C	-	0.94	-	V
			T _j =175°C	-	1.44	-	V
		I _D =200A V _{GS} =+18V	T _j =25°C	-	0.84	1.26	V
			T _j =175°C	-	1.26	-	V
C _{iss}	Input Capacitance	V _D =800V, V _{GS} =0V f =1MHz, V _{AC} =25mV		-	23.2	-	nF
C _{oss}	Output Capacitance			-	0.704	-	nF
C _{rss}	Reverse transfer Capacitance			-	0.056	-	nF
Q _G	Total gate charge	V _{DD} =800V, I _D =240A, V _{GS} =-5/+15V		-	720	-	nC
R _{Gint}	Internal Gate Resistance	f =1Mhz, V _{AC} =25mV		-	0.33	-	Ω
t _{d(on)}	Turn-on delay time	V _{DD} =600V I _D =200A V _{GS} =-5/+15V R _G =2.5Ω Inductive load switching operation	T _j =25°C	-	31	-	ns
			T _j =150°C	-	30	-	
t _r	Rise time		T _j =25°C	-	17	-	ns
			T _j =150°C	-	18	-	
t _{d(off)}	Turn-off delay time		T _j =25°C	-	101	-	ns
			T _j =150°C	-	119	-	
t _f	Fall time		T _j =25°C	-	38	-	ns
			T _j =150°C	-	44	-	
E _{on}	Turn-on power dissipation		T _j =25°C	-	4.42	-	mJ
			T _j =150°C	-	4.67	-	
E _{off}	Turn-off power dissipation		T _j =25°C	-	1.76	-	mJ
			T _j =150°C	-	1.97	-	
R _{th(j-c)}	FET Thermal Resistance	Junction to Case/MOSFET		-	0.07	-	K/W
R _{th(c-f)}	Contact thermal resistance	With thermal conductive grease /MOSFET		-	0.12	-	K/W

Assumes Thermal Conductivity of grease is 2.8 W/m · K and thickness is 50um.

Symbol	Item	Condition		Value			Unit
				Min.	Typ.	Max	
V _{SD}	Body Diode Forward Voltage	V _{GS} =-5V I _{SD} =200A	T _j =25°C	-	5.17	-	V
			T _j =175°C	-	4.66	-	
T _{rr}	Reverse recovery time	V _{DD} =600V I _D =200A	T _j =25°C	-	58	-	ns
			T _j =150°C	-	48	-	
Q _{rr}	Reverse recovery charge	V _{GS} =-5/+15V R _G =2.5Ω	T _j =25°C	-	4.41	-	μC
			T _j =150°C	-	9.02	-	
E _{rr}	Diode switching power dissipation	Inductive load switching operation	T _j =25°C	-	0.96	-	mJ
			T _j =150°C	-	1.90	-	

Figure 10 consists of three vertically stacked waveforms sharing a common time axis t .

- Top waveform:** Gate voltage V_{gs} vs. t . It shows a trapezoidal pulse with a 90% duty cycle. The pulse rises from 0V to a plateau, stays there for 90% of the period, and then falls back to 0V. The plateau level is indicated by a horizontal line labeled V_{gs} .
- Middle waveform:** Drain current I_d vs. t . It shows a trapezoidal pulse with a 90% duty cycle and 10% ripple. The pulse rises from 0A to a plateau, stays there for 90% of the period, and then falls back to 0A. The plateau level is indicated by a horizontal line labeled I_d . The ripple is indicated by a horizontal line labeled 10%.
- Bottom waveform:** Source current I_s vs. t . It shows a trapezoidal pulse with a 90% duty cycle and 10% ripple. The pulse rises from 0A to a plateau, stays there for 90% of the period, and then falls back to 0A. The plateau level is indicated by a horizontal line labeled I_s . The ripple is indicated by a horizontal line labeled 10%.

Key parameters and labels in the waveforms include:

- V_{gs} : Gate voltage
- $0V$: Zero voltage
- I_d : Drain current
- $0A$: Zero current
- I_s : Source current
- $0.1 \cdot I_{rRM}$: 10% ripple current
- $t_{d(on)}$: Delay time from gate voltage rise to drain current rise
- t_r : Rise time of drain current
- $t_{d(off)}$: Delay time from gate voltage fall to drain current fall
- t_f : Fall time of drain current
- t_{rr} : Reverse recovery time of the diode
- Q_{rr} : Integration of I_{rr} during t_{rr}

5/10

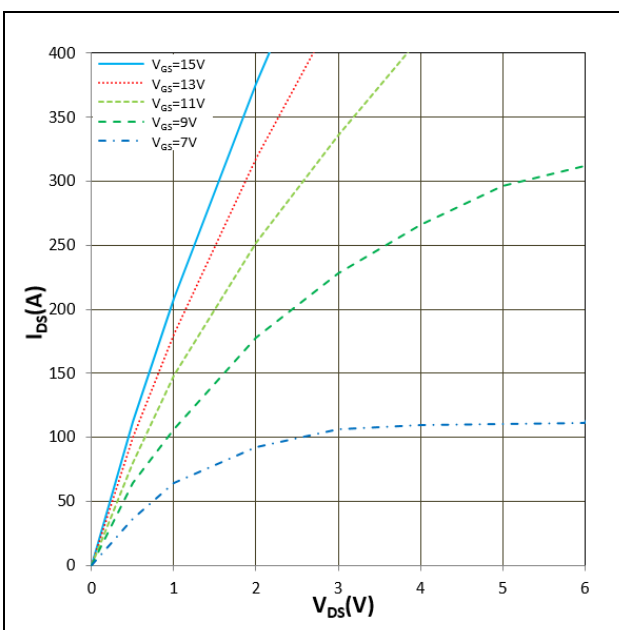


Figure 5. I_{DS} vs V_{DS}
 $T_j = 25^\circ\text{C}$, V_{GS} parameter

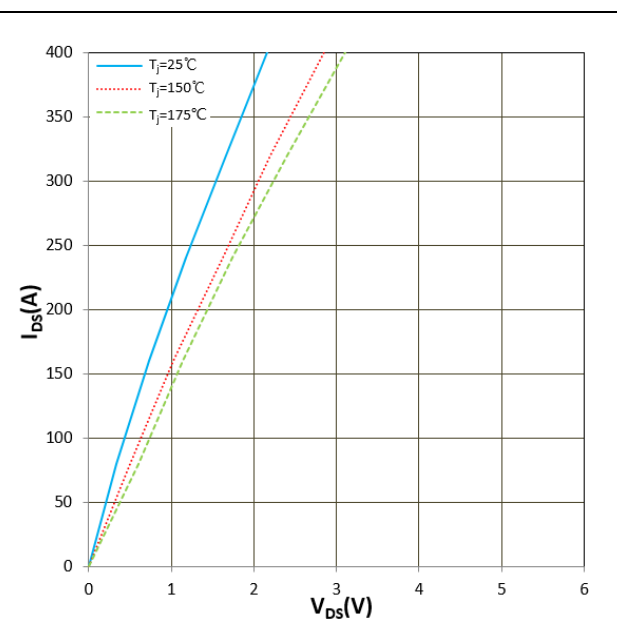


Figure 6. I_{DS} vs V_{DS}
 $V_{GS} = +15\text{V}$

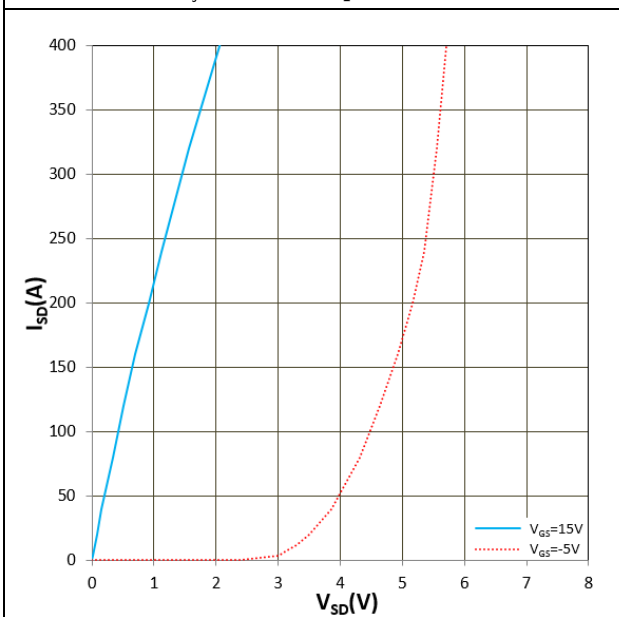


Figure 7. I_{SD} vs V_{SD} (V_F)
 $T_j = 25^\circ\text{C}$, V_{GS} parameter

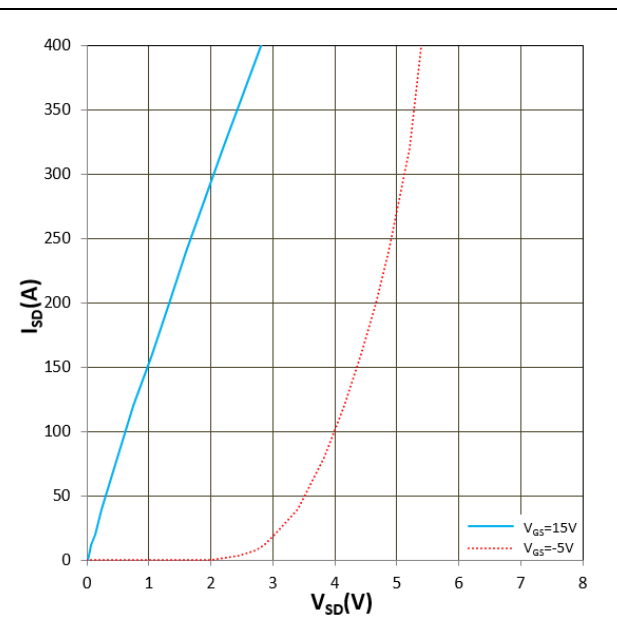


Figure 8. I_{SD} vs V_{SD} (V_F)
 $T_j = 175^\circ\text{C}$, V_{GS} parameter

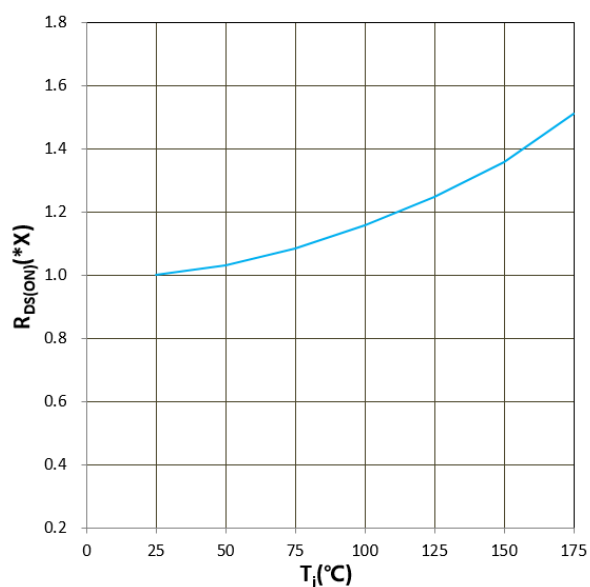


Figure 9. $R_{DS(ON)}$ vs T_j
 $V_{GS} = +15\text{V}$, $I_D = 200\text{A}$, $1.0X = 4.7\text{m}\Omega$

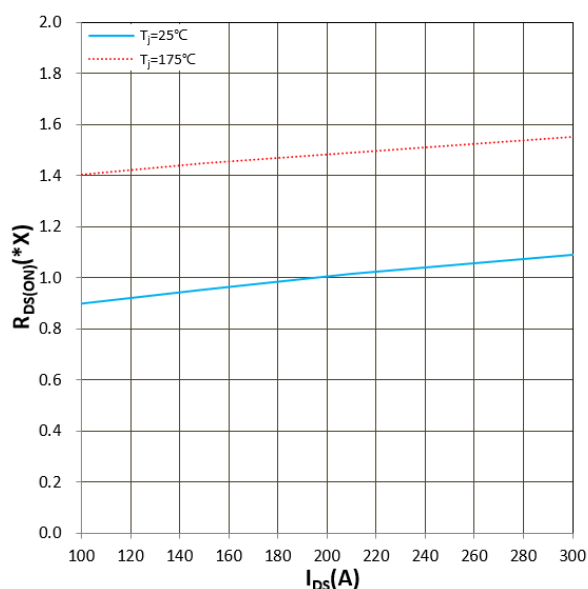


Figure 10. $R_{DS(ON)}$ vs I_{DS}
 $V_{GS} = +15\text{V}$, $1.0X = 4.7\text{m}\Omega$

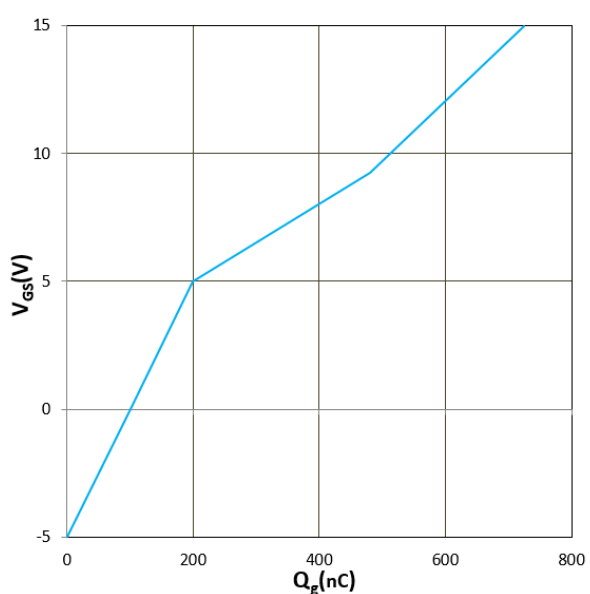


Figure 11. V_{GS} vs Q_g
 $V_{DS} = 800\text{V}$, $I_D = 240\text{A}$, $T_j = 25^{\circ}\text{C}$

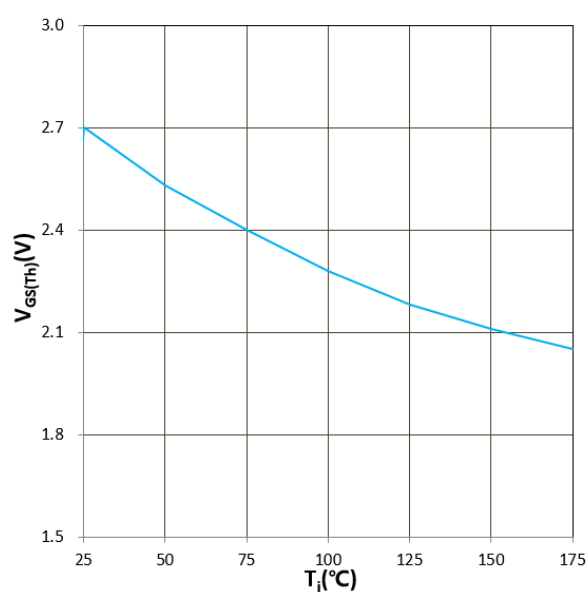


Figure 12. $V_{GS(TH)}$ vs T_j
 $V_{GS} = V_{DS}$, $I_D = 140\text{mA}$

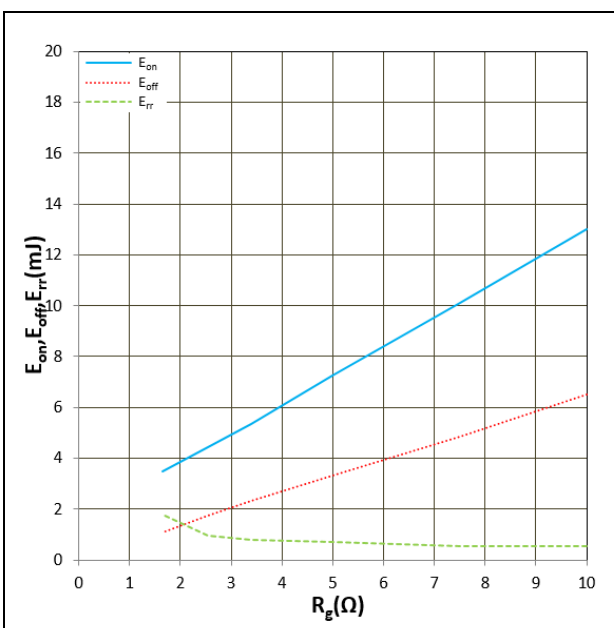


Figure 13. E_{on} , E_{off} , E_{rr} vs R_g
 $T_j = 25^\circ\text{C}$, $V_{CC} = 600\text{V}$, $I_D = 200\text{A}$, $V_{GS} = -5\text{V}/+15\text{V}$
 Inductive Load

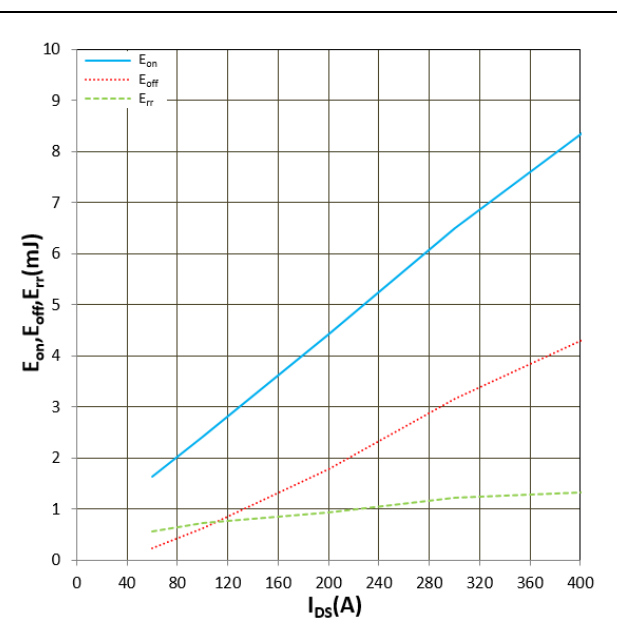


Figure 14. E_{on} , E_{off} , E_{rr} vs I_{DS}
 $T_j = 25^\circ\text{C}$, $V_{CC} = 600\text{V}$, $R_g = 2.5\Omega$, $V_{GS} = -5\text{V}/+15\text{V}$
 Inductive Load

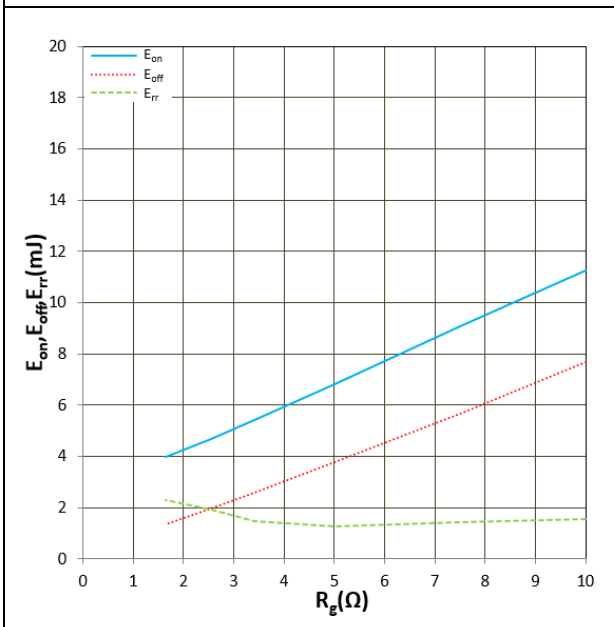


Figure 15. E_{on} , E_{off} , E_{rr} vs R_g
 $T_j = 150^\circ\text{C}$, $V_{CC} = 600\text{V}$, $I_D = 200\text{A}$, $V_{GS} = -5\text{V}/+15\text{V}$
 Inductive Load

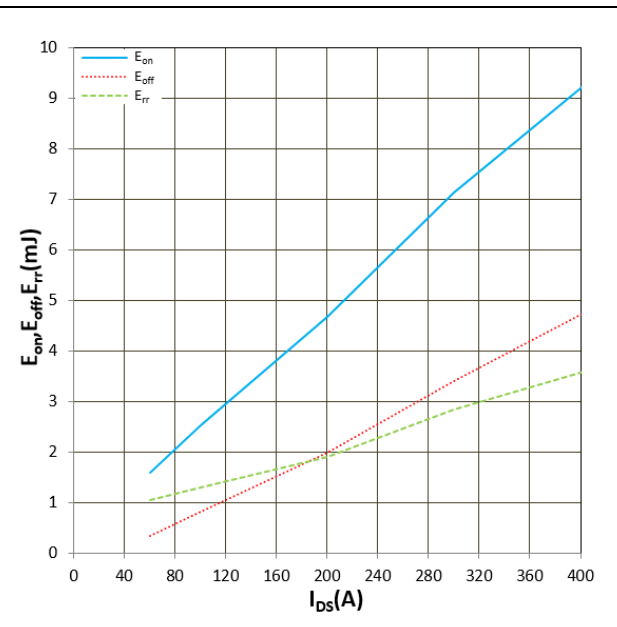
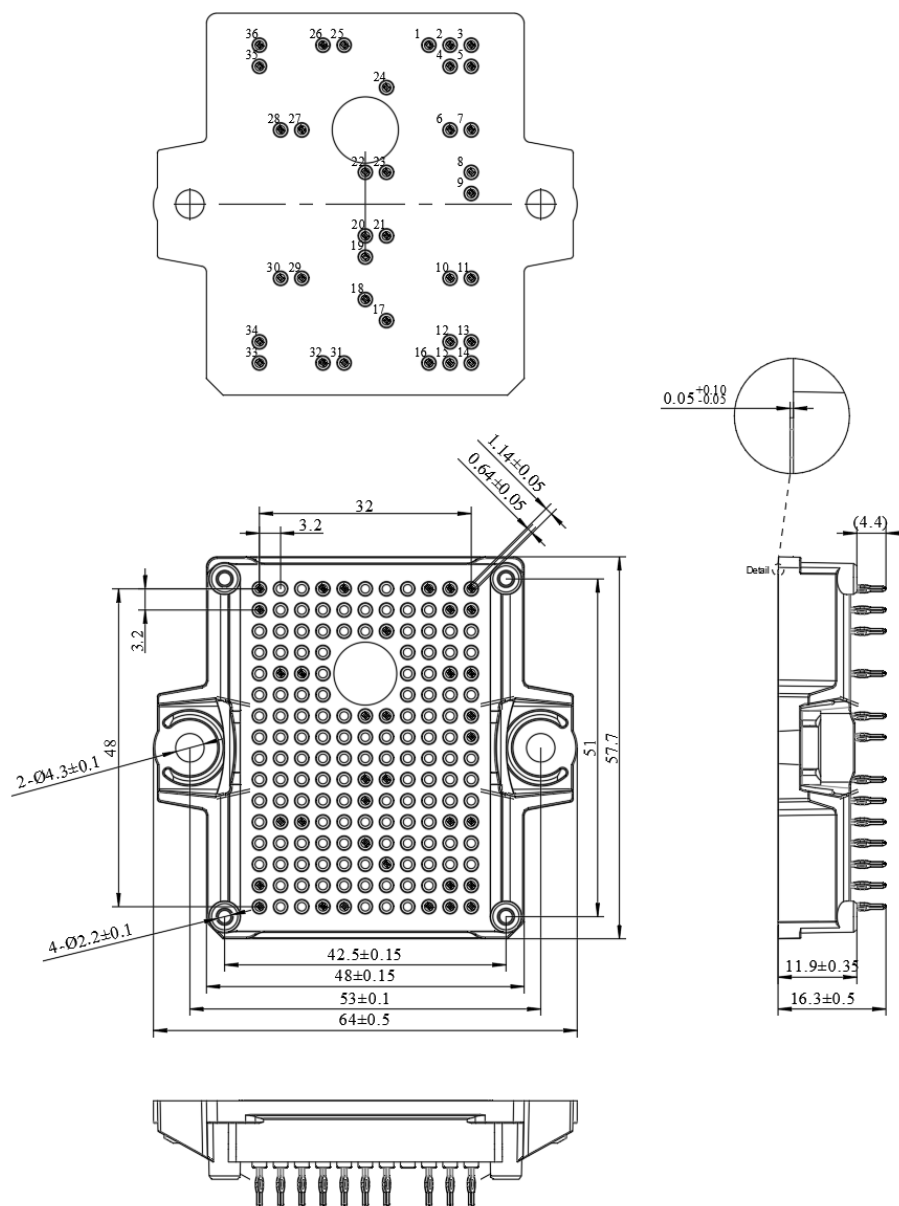


Figure 16. E_{on} , E_{off} , E_{rr} vs I_{DS}
 $T_j = 150^\circ\text{C}$, $V_{CC} = 600\text{V}$, $R_g = 2.5\Omega$, $V_{GS} = -5\text{V}/+15\text{V}$
 Inductive Load

Package dimensions



Pin	X	Y
1	25,6	48
2	28,8	48
3	32	48
4	28,8	44,8
5	32	44,8
6	28,8	35,2
7	32	35,2
8	32	28,8
9	32	25,6
10	28,8	12,8
11	32	12,8
12	28,8	3,2
13	32	3,2
14	32	0
15	28,8	0
16	25,6	0
17	19,2	6,4
18	16	9,6
19	16	16
20	16	19,2
21	19,2	19,2
22	16	28,8
23	19,2	28,8
24	19,2	41,6
25	12,8	48
26	9,6	48
27	6,4	35,2
28	3,2	35,2
29	6,4	12,8
30	3,2	12,8
31	12,8	0
32	9,6	0
33	0	0
34	0	3,2
35	0	44,8
36	0	48

Unit: mm

IMPORTANT NOTICE

This product data sheet describes the characteristics of this product for which a warranty is granted. Any such warranty is granted exclusively under the terms and conditions of the supply agreement. There will be no guarantee or of any kind for the product and its characteristics.

The data contained in this document is exclusively intended for technically trained staff. You and your technical departments will have to evaluate the product's suitability for the intended application and the completeness of the product data concerning such application.

Due to technical requirements, our product may contain dangerous substances. For information on the types in question, please contact the sales staff responsible for you.

Changes to this product data sheet are reserved.

Please contact the sales staff (Sales@leapers-power.com) for further information on the product, technology, delivery terms, conditions and prices.