

Description

The DFH08TL12EZE2A is a 3 level Power Module. It integrates 1200V SiC MOSFET chips and 650V IGBT chips designed for the applications such as Solar Inverter, Energy storage Systems, DC/DC, etc.



Features

- 1200V/8mΩ SiC MOSFET
- 650V/200A IGBT
- SiC SBD
- Low Switching Losses
- High current density
- Press FIT Contact Technology
- Pre-applied thermal interface material
- Thermistor inside

Applications

- Solar inverter Systems
- DC/DC converter
- Energy Storage Systems

Circuit diagram

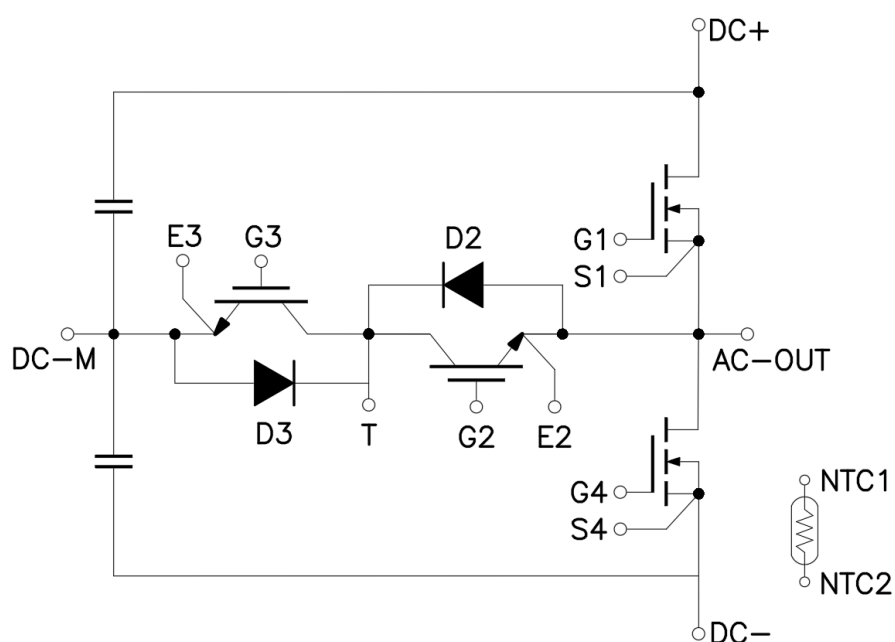


Figure 1. Out drawing & circuit diagram for DFH08TL12EZE2A

Pin Configuration and Marking Information

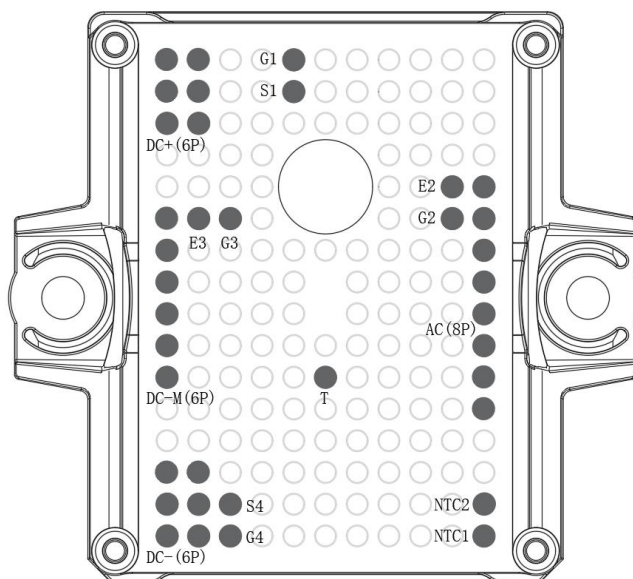


Figure 2. Pin configuration

Module

Parameter	Conditions	Value	Unit
Isolation Voltage	RMS, f =50Hz, t =1min	3.0	kV
Clearance	Terminal to Terminal	5.0	mm
	Terminal to Heatsink	10.0	mm
Creepage distance	Terminal to Terminal	6.3	mm
	Terminal to Heatsink	12.7	mm
Comparative Tracking Index	-	> 400	-
T _{stg}	Storage temperature	-40 to 125	°C
Weight	-	40	g

NTC characteristics

Symbol	Parameter	Condition	Value			Unit
			Min.	Typ.	Max.	
R ₂₅	Resistance	T _C =25°C	-	5	-	kΩ
ΔR/R	Deviation of R100	T _C =100°C, R ₁₀₀ =493Ω	-5	-	5	%
P ₂₅	Power dissipation	T _C =25°C	-	-	20	mW
B _{25/50}	B-value	R ₂ =R ₂₅ exp [B _{25/50} (1/T ₂ - 1/(298,15 K))]	-	3375	-	K
B _{25/80}	B-value	R ₂ =R ₂₅ exp [B _{25/80} (1/T ₂ - 1/(298,15 K))]	-	3411	-	K
B _{25/100}	B-value	R ₂ =R ₂₅ exp [B _{25/100} (1/T ₂ - 1/(298,15 K))]	-	3433	-	K

Capacitor (DC)

Symbol	Parameter	Conditions	Value	Unit
T _{OP}	Operation Temperature	-	-40 to 150	°C
C	Capacitance	DC bias voltage = 0V, 25°C	150	nF
V	DC voltage ratings	-	630	V

Maximum Ratings (SiC MOSFET, T_j=25°C unless otherwise specified)

Symbol	Parameter	Conditions	Ratings	Unit
V _{DSS}	Drain-Source Voltage	25° C ≤ T _j ≤ 175° C	1200	V
V _{GSS}	G-S Voltage	D-S Short, Note1	-8 to 22	V
I _{DS}	DC Continuous Drain Current	T _s = 80°C	160	A
I _{DP}	Drain Pulse Current, Peak	Less than 1ms, Note2	400	A
T _j	junction temperature	-	-55 to 175	°C

Note1: Recommended Operating Value, +18V/-4V, +15V/-4V

Note2: Pulse width limited by maximum junction temperature

Maximum Ratings (IGBT, 3-Level, T_j=25°C unless otherwise specified)

Symbol	Parameter	Conditions	Ratings	Unit
V _{CES}	Collector-Emitter Voltage	G-E Short	650	V
V _{GES}	Gate-Emitter Voltage	C-E Short	±20	V
	Transient gate emitter voltage, T _P ≤ 10μs, D < 0.01	C-E Short	±30	
I _C	DC Continuous Collector Current	T _s = 80°C	190	A
I _{CM}	Pulse Collector Current	t _p = 1ms, Note1	400	A
T _j	junction temperature	-	-55 to 175	°C

Note1: Pulse width limited by maximum junction temperature

Maximum Ratings (SiC SBD, 3-Level, T_j=25°C unless otherwise specified)

Symbol	Parameter	Conditions	Ratings	Unit
V _{RRM}	Repetitive peak reverse Voltage	-	650	V
I _F	Diode forward Current	T _s = 80°C	130	A
I _{FRM}	Repetitive peak forward Current	t _p = 1ms, Note1	300	A
I ² t	I ² t for fusing	sine-wave pulse, t _p = 10ms	924.5	A ² s
T _j	junction temperature	-	-55 to 175	°C

Note1: Pulse width limited by maximum junction temperature

SiC MOSFET Electrical characteristics (SiC MOSFET, $T_j=25^{\circ}\text{C}$ unless otherwise specified, chip)

Symbol	Item	Condition		Value			Unit
				Min.	Typ.	Max	
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =0.2mA		1200	-	-	V
I _{DSS}	Zero gate voltage drain Current	V _{DS} =1200V, V _{GS} =0V		-	-	200	μA
V _{GS(th)}	Gate-Source threshold Voltage	I _D =40mA, V _{DS} =V _{GS}		1.9	2.6	3.5	V
I _{GSS}	Gate-Source Leakage Current	V _{GS} =18V, V _{DS} =0V		-	-	200	nA
R _{DS(on)} (Chip)	Static drain-source	I _D =100A	T _j =25°C	-	8	13	mΩ
	On-state resistance	V _{GS} =18V	T _j =175°C	-	14	-	mΩ
V _{DS(on)} (Chip)	Static drain-source	I _D =100A	T _j =25°C	-	0.8	1.45	V
	On-state Voltage	V _{GS} =18V	T _j =175°C	-	1.4	-	V
C _{iss}	Input Capacitance	V _{DS} =1000V, V _{GS} =0V, f =1MHz		-	9400	-	pF
C _{oss}	Output Capacitance			-	400	-	pF
C _{rss}	Reverse transfer Capacitance			-	40	-	pF
Q _G	Total gate charge	V _{DD} =800V, I _D =100A, V _{GS} =0/+18V		-	430	-	nC
R _{Gint}	Internal Gate Resistance	f =1Mhz		-	0.3	-	Ω
t _{d(on)}	Turn-on delay time	V _{DD} =400V I _D =100A V _{GS} =+18/-4V R _{GON} =5.0Ω R _{GOFF} =3.3Ω Inductive load switching operation	T _j =25°C	-	28	-	ns
			T _j =150°C	-	26	-	
t _r	Rise time		T _j =25°C	-	41	-	ns
			T _j =150°C	-	44	-	
t _{d(off)}	Turn-off delay time		T _j =25°C	-	66	-	ns
			T _j =150°C	-	70	-	
t _f	Fall time		T _j =25°C	-	23	-	ns
			T _j =150°C	-	24	-	
E _{on}	Turn-on power dissipation		T _j =25°C	-	1.44	-	mJ
			T _j =150°C	-	1.87	-	
E _{off}	Turn-off power dissipation	T _j =25°C	-	0.54	-	mJ	
		T _j =150°C	-	0.67	-		
R _{th(j-s)}	Thermal Resistance, Junction to sink (Conductive Grease applied), Note1			-	0.24	-	K/W

Note1: Assumes Thermal Conductivity of grease is $2.8\text{W/m}\cdot\text{K}$ and thickness is $50\mu\text{m}$.

Body Diode Electrical characteristics ($T_j=25^{\circ}\text{C}$ unless otherwise specified, chip: Target)

Symbol	Item	Condition		Value			Unit
				Min.	Typ.	Max	
I_{SD}	DC body diode forward Current	$V_{GS}=-4\text{V}$	$T_s\leq 80^{\circ}\text{C}$	-	-	80	A
V_{SD}	Body Diode Forward Voltage	$V_{GS}=-4\text{V}$	$T_j=25^{\circ}\text{C}$	-	4.7	-	V
		$I_{SD}=100\text{A}$	$T_j=175^{\circ}\text{C}$	-	4.2	-	

IGBT (3-Level) Electrical characteristics (T_j=25°C unless otherwise specified, chip)

Symbol	Item	Condition		Value			Unit
				Min.	Typ.	Max	
V _{CE(sat)} (Chip)	Collector-Emitter Saturation Voltage	I _C =100A V _{GE} =15V	T _J =25°C	-	1.35	1.6	V
			T _J =150°C		1.50	-	V
			T _J =175°C		1.60	-	V
V _{GE(th)}	Gate-Emitter threshold Voltage	I _C =1mA, V _{CE} =V _{GE}		3.5	4.5	5.5	V
Q _G	Total gate charge	V _{GE} =15V, V _{CC} =520V, I _C =160A		-	348	-	nC
I _{CES}	Collector- Emitter Cut off Current	V _{CE} =650V, V _{GE} =0V		-	-	20	μA
I _{GES}	Gate-Emitter Leakage Current	V _{GE} =20V, V _{CE} =0V		-	-	0.2	μA
C _{iss}	Input Capacitance	V _{CE} =25V, V _{GE} =0V, f =100KHz		-	17296	-	pF
C _{oes}	Output Capacitance			-	306	-	pF
C _{res}	Reverse transfer Capacitance			-	9.8	-	pF
t _{d(on)}	Turn-on delay time	V _{CC} =400V I _C =100A V _{GE} =+15/-8V R _{Gon} = R _{Goff} =5Ω Inductive load	T _J =25°C	-	81	-	ns
			T _J =150°C	-	85	-	
t _r	Rise time		T _J =25°C	-	74	-	ns
			T _J =150°C	-	77	-	
t _{d(off)}	Turn-off delay time		T _J =25°C	-	230	-	ns
			T _J =150°C	-	249	-	
t _f	Fall time		T _J =25°C	-	20	-	ns
			T _J =150°C	-	42	-	
E _{on}	Turn-on power dissipation		T _J =25°C	-	2.6	-	mJ
			T _J =150°C	-	5.5	-	
E _{off}	Turn-off power dissipation		T _J =25°C	-	0.78	-	mJ
			T _J =150°C	-	1.33	-	
R _{th(j-s)}	Thermal Resistance, Junction to sink (Conductive Grease applied), Note1			-	0.25	-	K/W

Note1: Assumes Thermal Conductivity of grease is 2.8W/m·K and thickness is 50μm.

SiC SBD (3-Level) Electrical characteristics (T_j=25°C unless otherwise specified, chip)

Symbol	Item	Condition		Value			Unit
				Min.	Typ.	Max	
V _F	Diode Forward Voltage	V _{GS} =-4V	T _j =25°C	-	1.40	1.70	V
		I _{SD} =100A	T _j =150°C	-	1.70	2.20	
C _d	Diode capacitance	f = 1MHz, V _R =1V	T _j =25°C	-	5864	-	pF
		f = 1MHz, V _R =400V		-	598	-	
I _R	Reverse Current	V _R =650V	T _j =25°C		8	600	μA
			T _j =175°C		120	2400	
Q _R	Recovered charge	V _R =400V, I _F =100A , di/dt=500A/μs	T _j =25°C	-	288	-	nC
E _{rr}	Reverse recovered energy	V _R =400V, I _F =100A, R _G =5.0Ω	T _j =25°C	-	0.44	-	mJ
		V _{GS} =+ 15/-8V, Inductive load	T _j =150°C	-	0.57	-	

$R_{th(j-s)}$	Thermal Resistance, Junction to sink (Conductive Grease applied), Note1	-	0.38	-	K/W
---------------	---	---	------	---	-----

Note1: Assumes Thermal Conductivity of grease is 2.8W/m·K and thickness is 50um.

Test Conditions

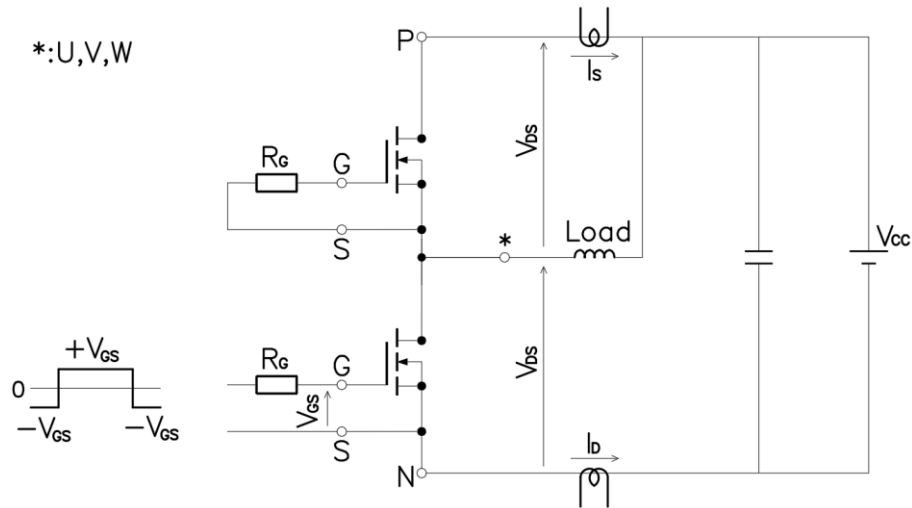


Figure 3. Switching time measure circuit

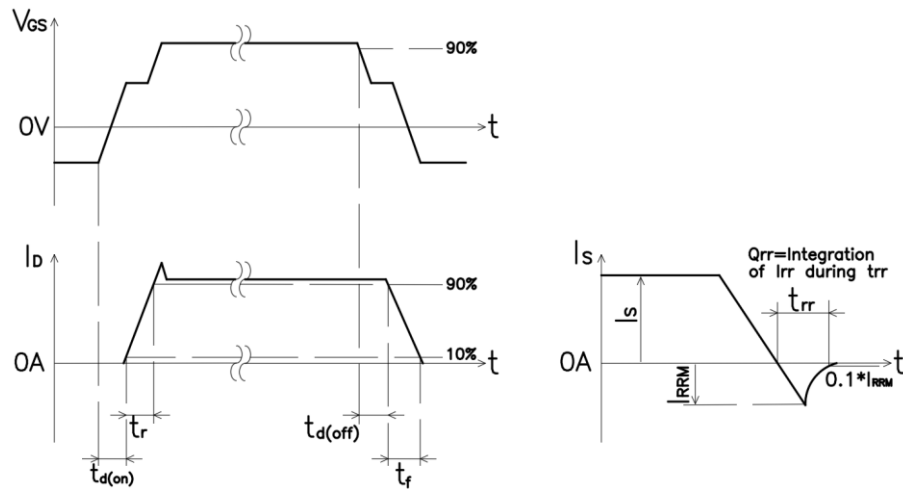


Figure 4. Switching time definition

SiC MOSFET, Body Diode

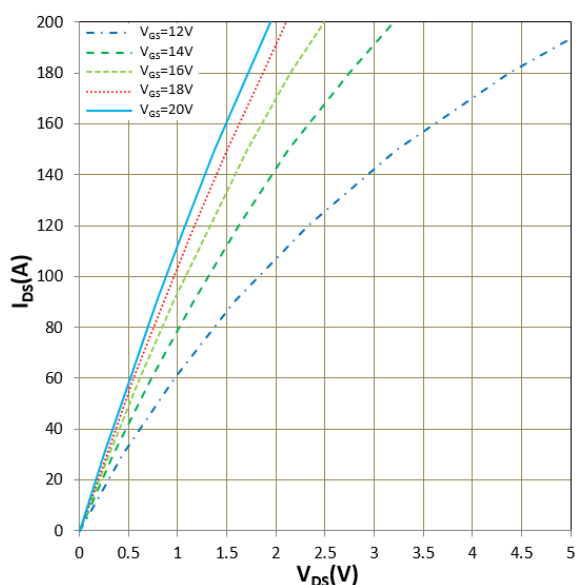


Figure 5. I_{DS} vs V_{DS}
 $T_J = 25^\circ\text{C}$

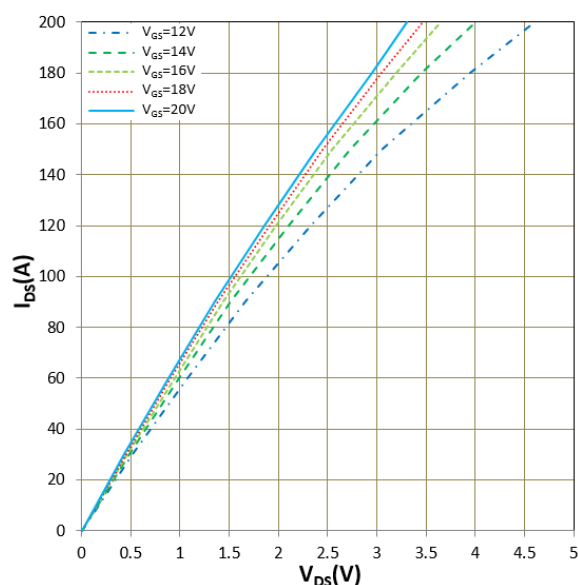


Figure 6. I_{DS} vs V_{DS}
 $T_J = 175^\circ\text{C}$

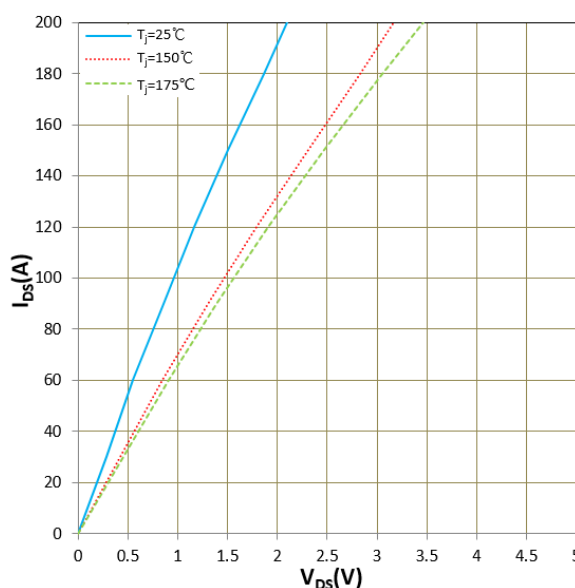


Figure 7. I_{DS} vs V_{DS}
 $V_{GS} = 18\text{V}$

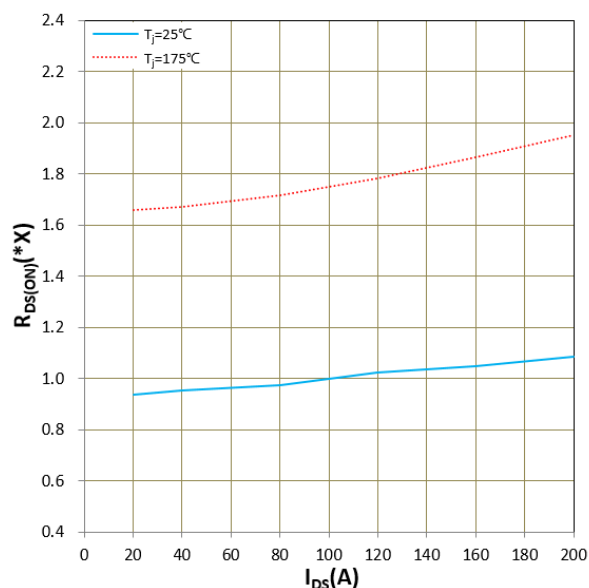


Figure 8. $R_{DS(on)}$ vs I_{DS}
 $1.0x = 8.0\text{m}\Omega$, $V_{GS} = 18\text{V}$

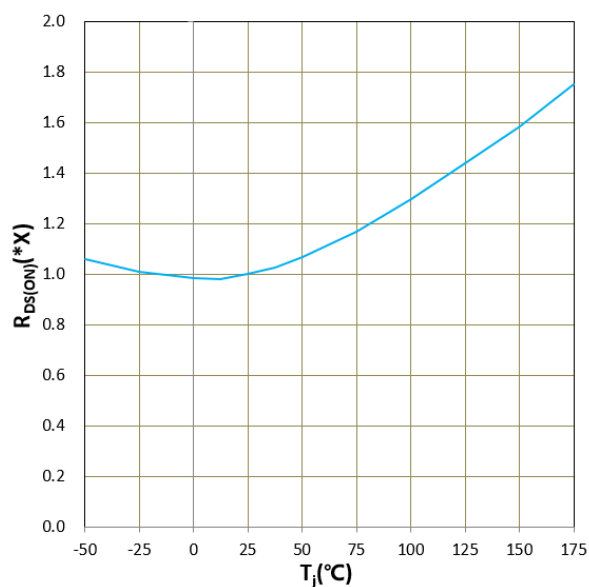


Figure 9. $R_{DS(on)}$ vs T_J
1.0x = 8.0mΩ, $I_D = 100A$

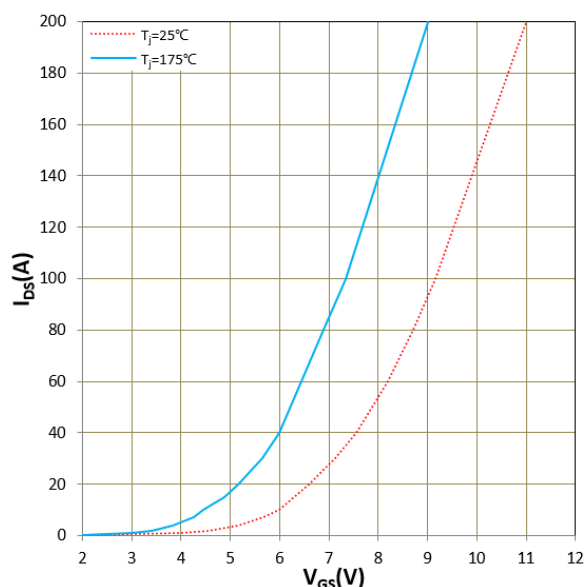


Figure 10. I_{DS} vs V_{GS}
 $V_{DS} = 10V$

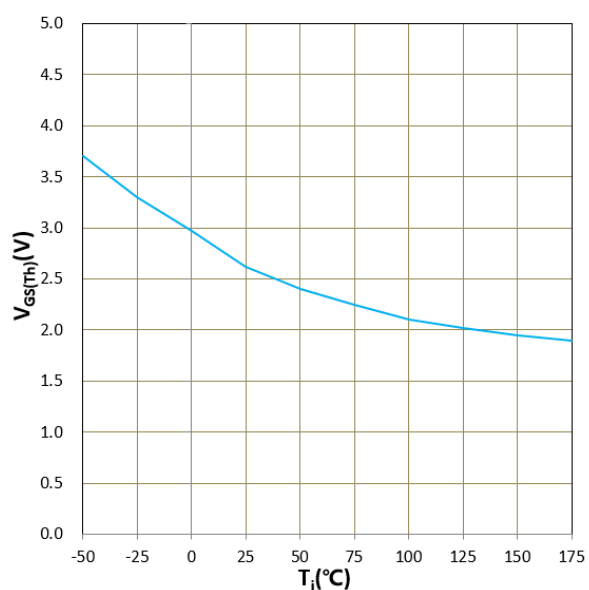


Figure 11. $V_{GS(th)}$ vs T_J
 $V_{DS} = 10V$, $I_{DS} = 40mA$

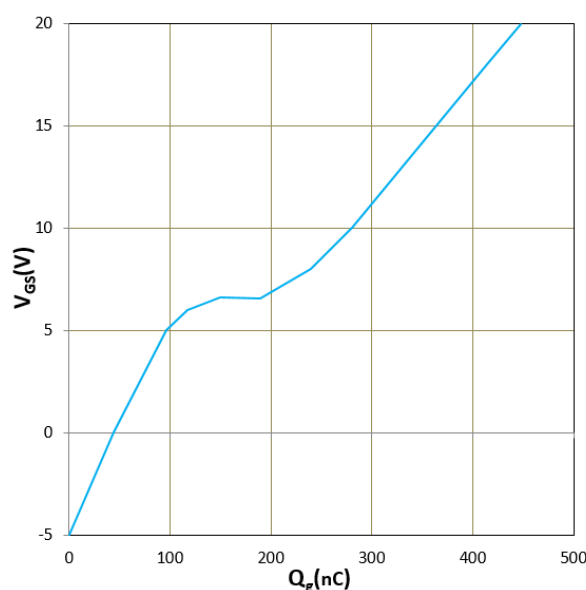


Figure 12. Gate charge
 $I_{DS} = 100A$, $I_{GS} = 0.2mA$, $V_{DS} = 400V$

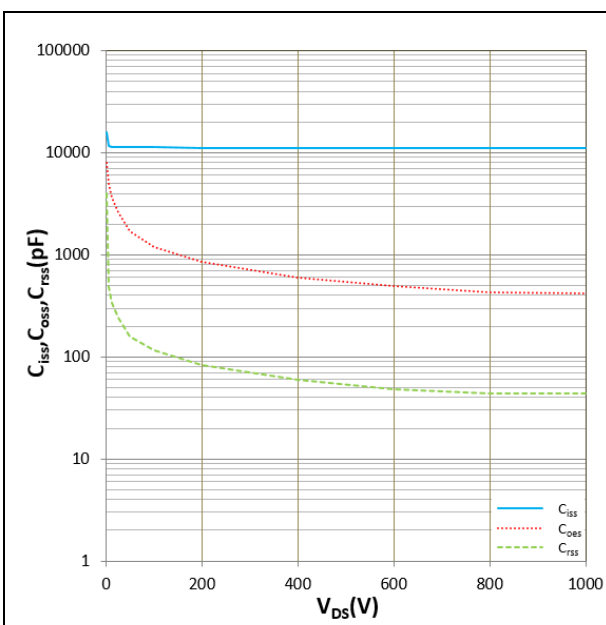


Figure 13. C_{iss}, C_{oss}, C_{rss} vs V_{CE}
V_{AC}=25mV, f =1MHz

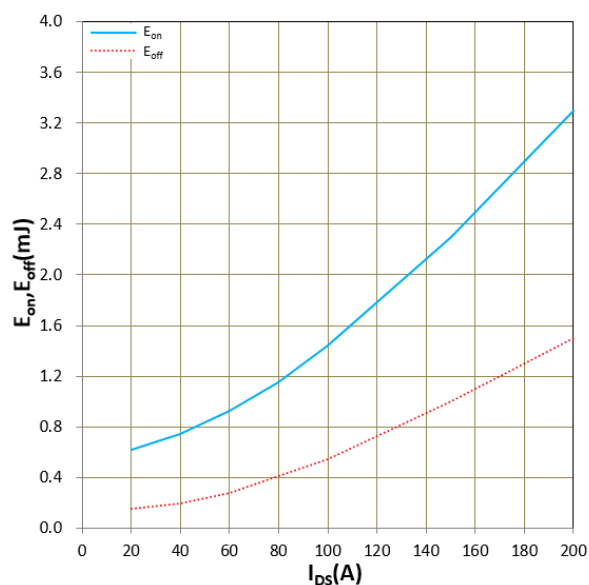


Figure 14. E_{on}, E_{off} vs I_{DS}(Typ)
T_j=25°C, V_{DD}=400V, V_{GE}=+18/-4V
R_{GON}=5.0Ω, R_{GOFF}=3.3Ω, Inductive Load

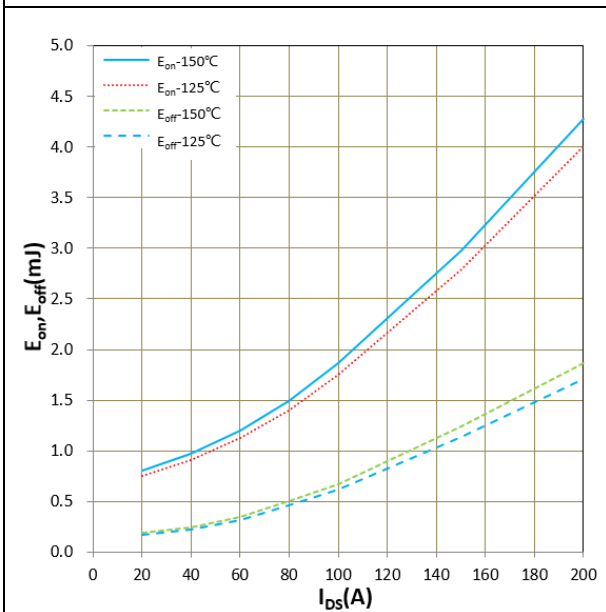


Figure 15. E_{on}, E_{off} vs I_{DS}(Typ)
T_j=125/150°C, V_{DD}=400V, V_{GE}=+18/-4V
R_{GON}=5.0Ω, R_{GOFF}=3.3Ω, Inductive Load

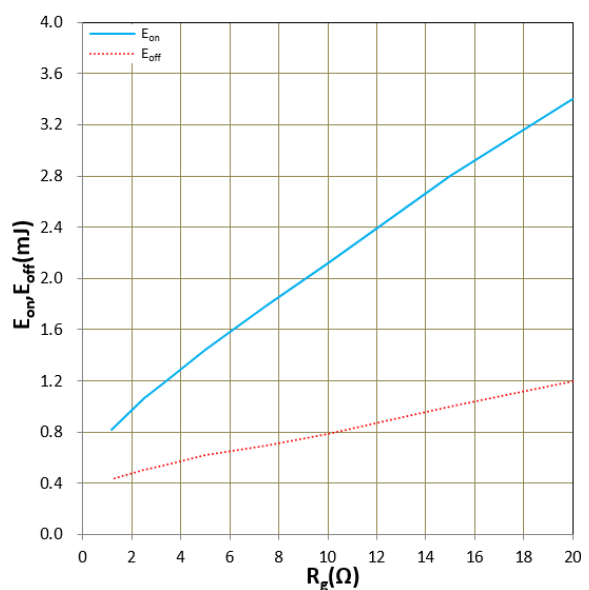


Figure 16. E_{on}, E_{off} vs R_g(Typ)
V_{DD}=400V, V_{GE}=+18/-4V, I_D=100A
T_j=25°C, Inductive Load

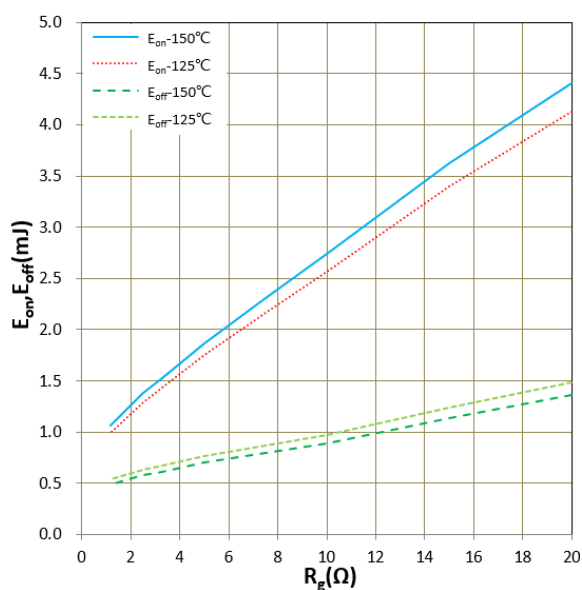


Figure 17. E_{on} , E_{off} vs R_g (Typ)
 $V_{DD}=400V$, $V_{GE}=+18/-4V$, $I_D=100A$,
 $T_j=125/150^{\circ}C$, Inductive Load

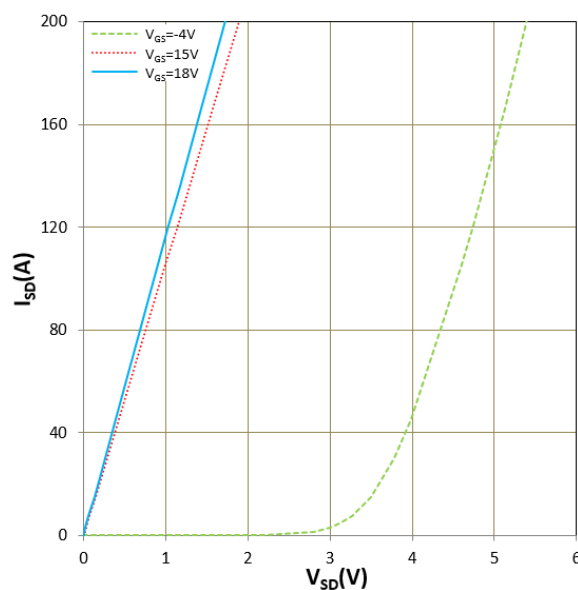


Figure 18. I_{SD} vs V_{SD}
 $T_j=25^{\circ}C$

IGBT, (3-Level)

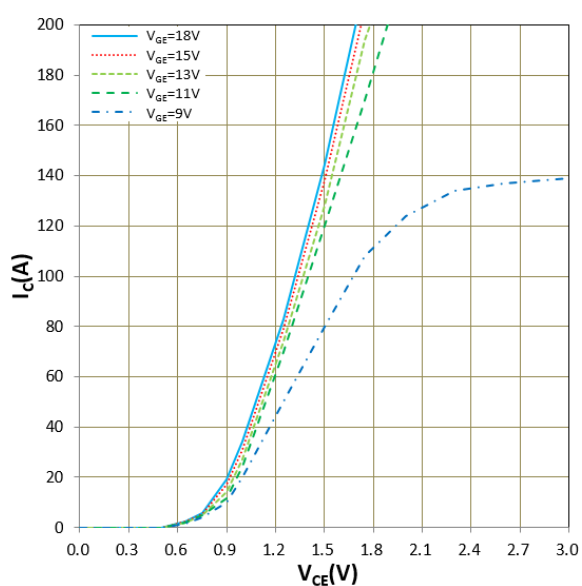


Figure 19. I_C vs V_{CE}
 $T_j=25^{\circ}C$

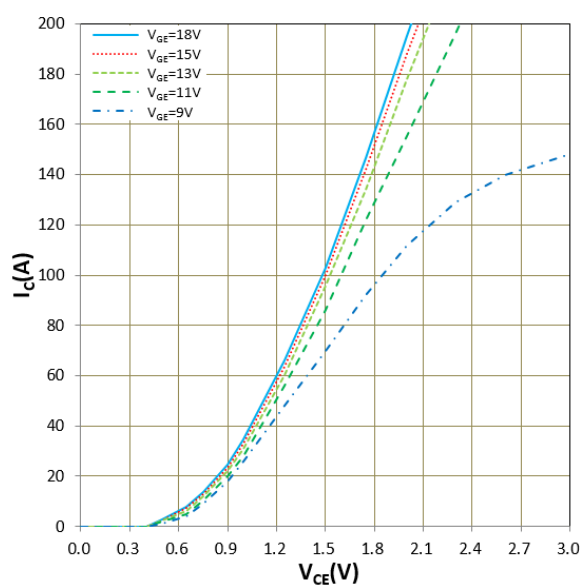


Figure 20. I_C vs V_{CE}
 $T_j=150^{\circ}C$

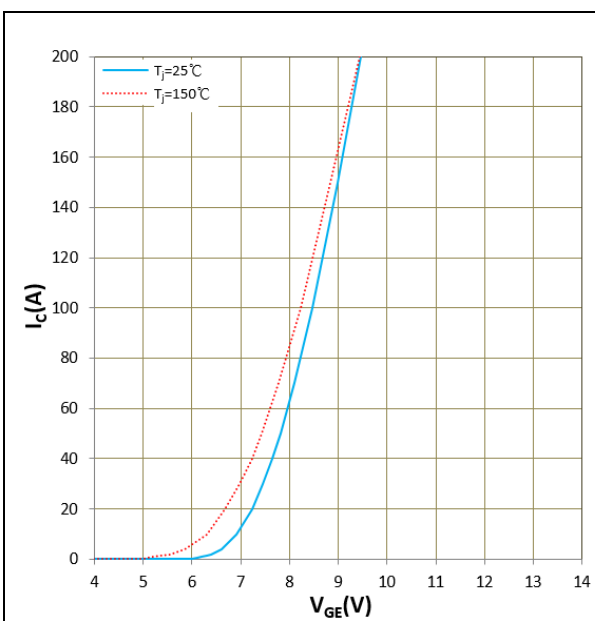


Figure 21. I_c vs V_{GE}
 $V_{CE}=20V$

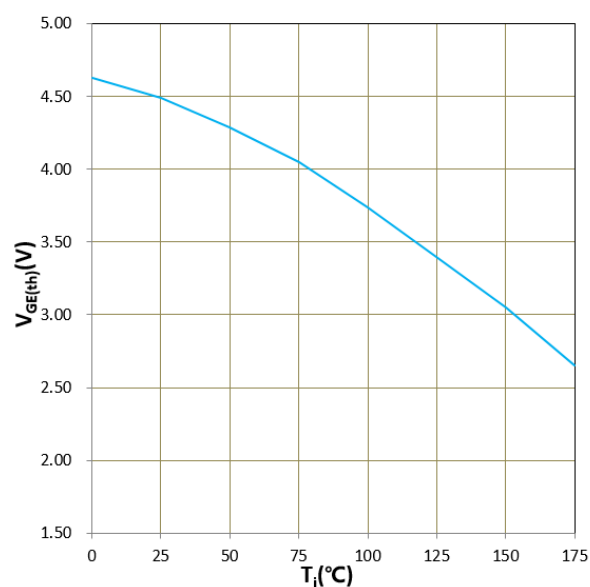


Figure 22. $V_{GE(th)}$ vs T_j
 $I_c=1mA$

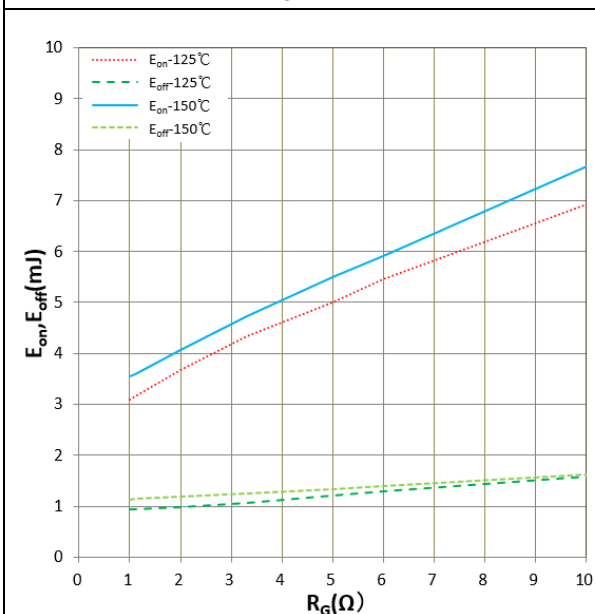


Figure 23. E_{on} , E_{off} vs R_G (Typ)
 $V_{CC}=400V$, $V_{GE}=+15V/-8V$, $I_c=100A$
Inductive Load

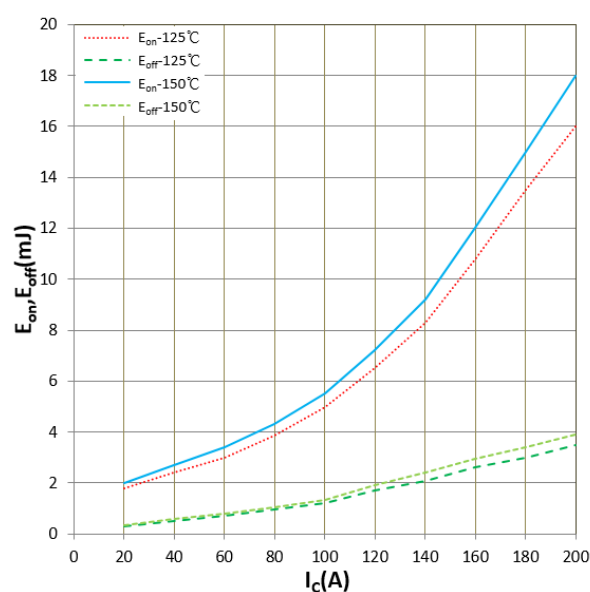


Figure 24. E_{on} , E_{off} vs I_c (Typ)
 $V_{CC}=400V$, $V_{GE}=+15V/-8V$, $R_G=5.0\Omega$
Inductive Load

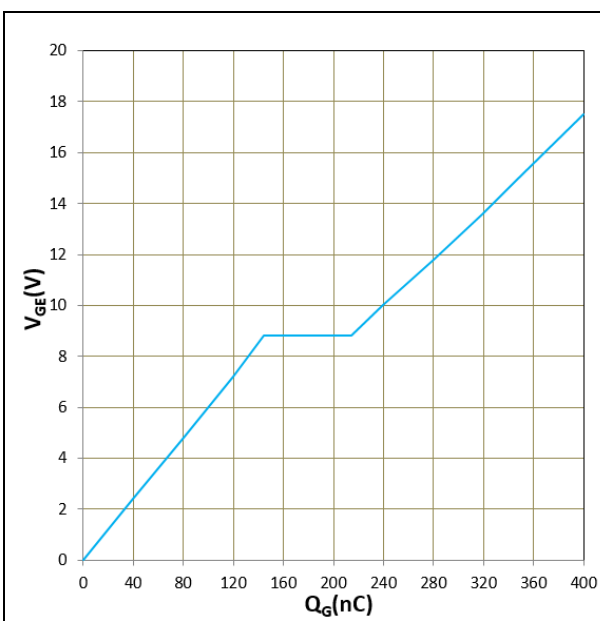


Figure 25. Gate charge
 $V_{CE} = 520V$, $I_C = 160A$

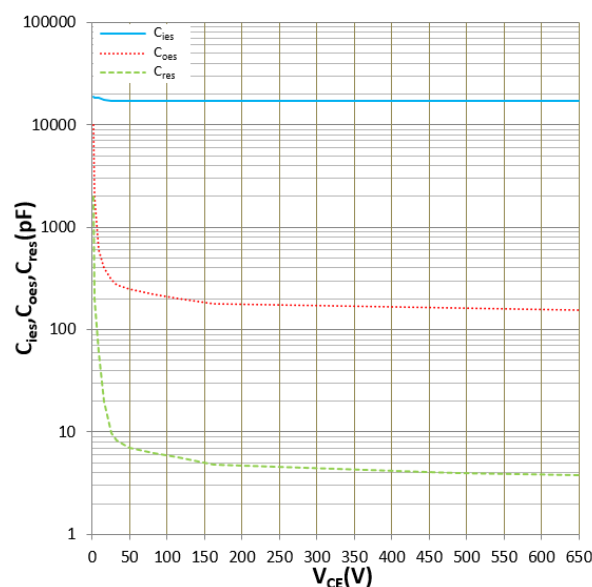


Figure 26. C_{ies} , C_{oes} , C_{res} vs V_{CE}
 $T_j = 25^\circ C$, $f = 100KHz$

SiC SBD, (3-Level)

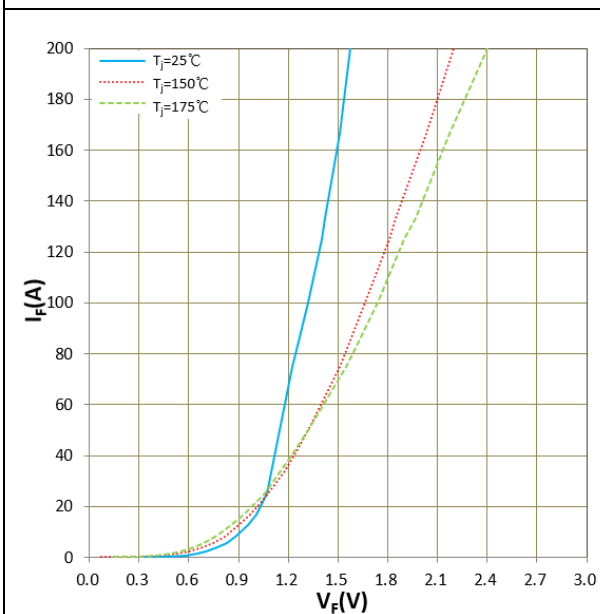


Figure 27. I_F vs V_F (Typ)

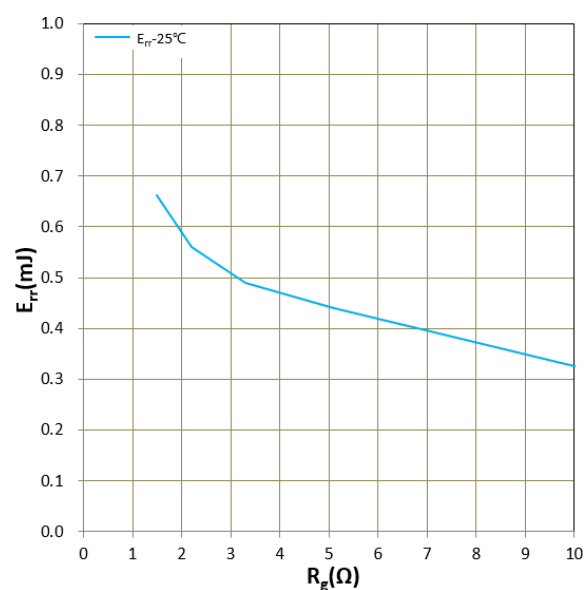


Figure 28. E_{rr} vs R_g (Typ)
 $V_R = 400V$, $I_F = 100A$, $V_{GE} = +15/-8V$
 $T_j = 25^\circ C$, Inductive Load

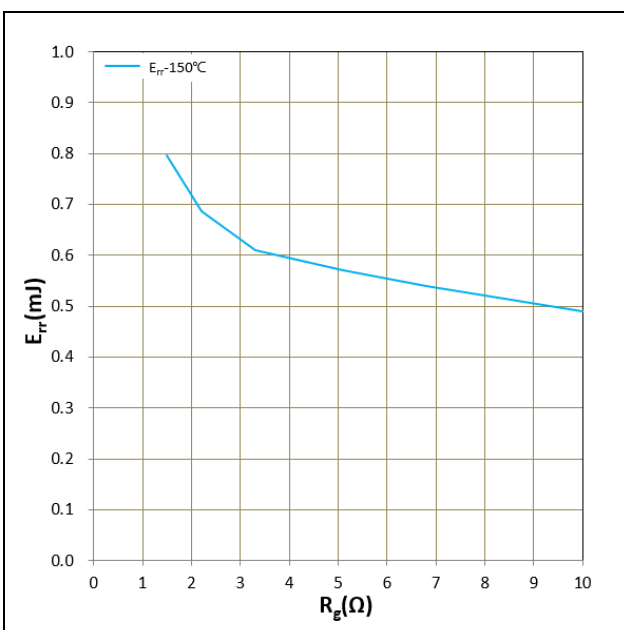


Figure 29. E_{rr} vs R_g (Typ)
 $V_R = 400\text{V}$, $I_F = 100\text{A}$, $V_{GE} = +15/-8\text{V}$
 $T_j = 150^\circ\text{C}$, Inductive Load

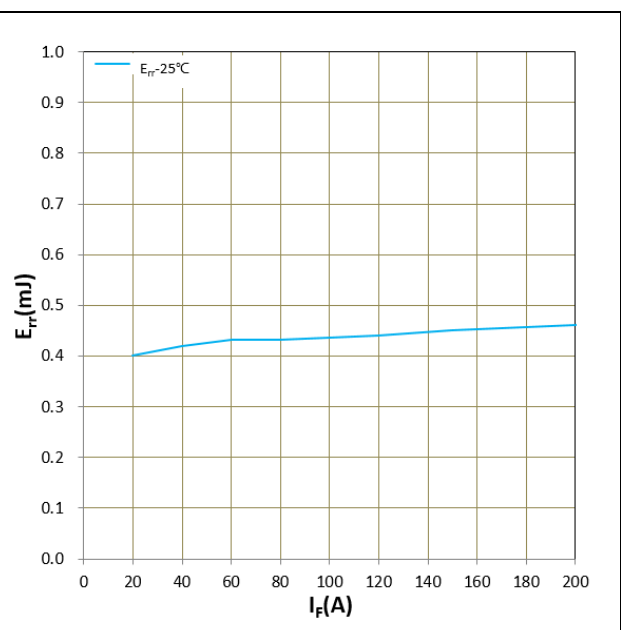


Figure 30. E_{rr} vs I_F (Typ)
 $V_R = 400\text{V}$, $V_{GE} = +15/-8\text{V}$, $R_G = 5.0\Omega$
 $T_j = 25^\circ\text{C}$, Inductive Load

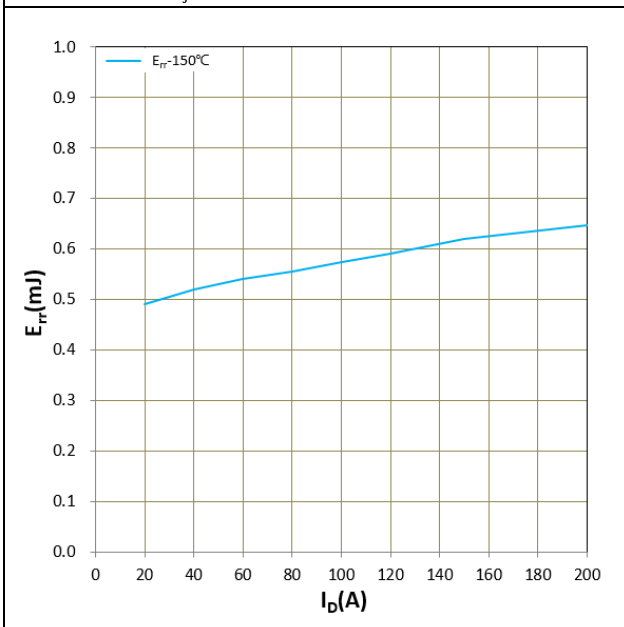


Figure 31. E_{rr} vs I_F (Typ)
 $V_R = 400\text{V}$, $V_{GE} = +15/-8\text{V}$, $R_G = 5.0\Omega$
 $T_j = 150^\circ\text{C}$, Inductive Load

