

Description

The DFS08HF12EZA2 is a Half Bridge SiC MOSFET half bridge Power Module. It integrates high performance SiC MOSFET chips designed for the applications such as Solar Inverter, UPS, Fuel cell- DC/DC converter, Energy storage Systems.



Features

- Blocking voltage:1200V
- $R_{ds(on)} = 7.5m\Omega$
- Low Switching Losses
- 175°C maximum junction temperature
- Thermistor inside

Applications

- Solar inverter Systems
- Fuel cell-DC/DC converter
- Uninterruptible Power Supplier
- Energy Storage Systems

Circuit diagram

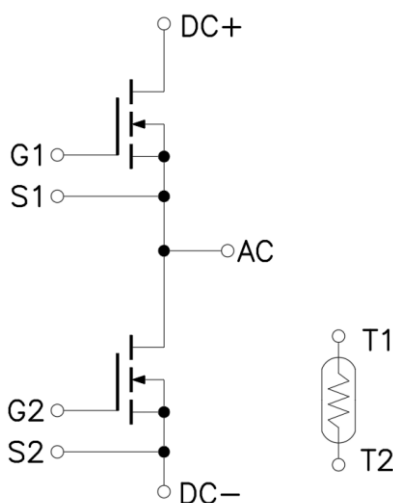


Figure 1. Out drawing & circuit diagram for DFS08HF12EZA2

Pin Configuration and Function Description

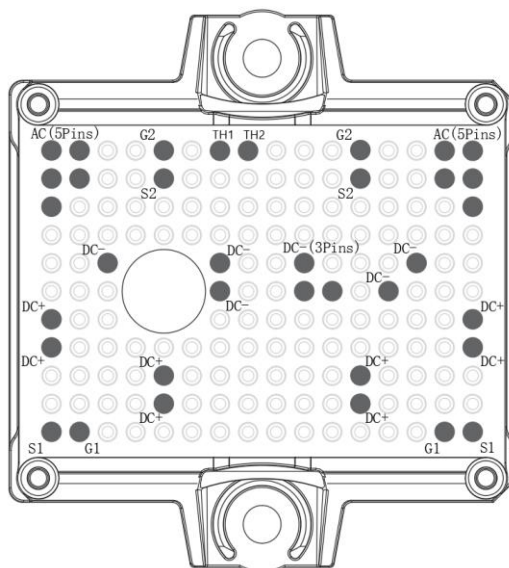


Figure 2. Pin configuration

PIN No.	Symbol	Description
1-5(5pins)	AC	Output terminal of half bridge
6	S2	Low side source signal terminal
7	G2	Low side gate signal terminal
8	TH1	Thermistor connection 1
9	TH2	Thermistor connection 2
10	S2	Low side source signal terminal
11	G2	Low side gate signal terminal
12-16(5pins)	AC	Output terminal of half bridge
17-24(8pins)	DC –	DC – Bus connection
25-32(8pins)	DC +	DC + Bus connection
33	S1	High side source signal terminal
34	G1	High side gate signal terminal
35	G1	High side gate signal terminal
36	S1	High side source signal terminal

Module

Parameter	Condition	Value	Unit
Isolation voltage	RMS, f =50Hz, t =1min	3.4	kV
Clearance	Terminal to Terminal	5	mm
	Terminal to Heatsink	10	mm
Creepage distance	Terminal to Terminal	6.3	mm
	Terminal to Heatsink	12.7	mm
Comparative Tracking Index	-	600	-

Maximum Ratings (T_j=25°C unless otherwise specified)

Symbol	Parameter	Condition	Ratings	Unit
V _{DSS}	Drain-Source Voltage	G-S Short	1200	V
V _{GSS}	Gate - Source Voltage (dynamic), T _{surge} <100ns	D-S Short, Note1	-8 to 19	V
V _{GSOP}	Gate - Source Voltage (static)	D-S Short, Note1	-4 to 15	
I _{DS}	DC Continuous Drain Current	T _f =90°C	160	A
I _{SD}	Source (Body diode) Current	T _f =90°C, with ON signal	160	A
I _{DP}	Drain Pulse Current, Peak	Less than 1ms, Note2	350	A
P _{tot}	Total Power Dissipation	T _C =25°C	1150	W
T _{jmax}	Max Junction Temperature	-	175	°C
T _{stg}	Storage temperature	-	-40 to 125	°C

Note1: Recommended Operating Value, -4V/+15V

Note2: Pulse width limited by maximum junction temperature

NTC characteristics

Symbol	Parameter	Condition	Value			Unit
			Min.	Typ.	Max.	
R ₂₅	Resistance	T _C =25°C	-	5	-	kΩ
ΔR/R	Deviation of R ₁₀₀	T _C =100°C, R ₁₀₀ =493Ω	-5	-	5	%
P ₂₅	Power dissipation	T _C =25°C	-	-	20	mW
B _{25/50}	B-value	R ₂ =R ₂₅ exp [B _{25/50} (1/T ₂ - 1/(298,15 K))]	-	3375	-	K
B _{25/80}	B-value	R ₂ =R ₂₅ exp [B _{25/80} (1/T ₂ - 1/(298,15 K))]	-	3411	-	K
B _{25/100}	B-value	R ₂ =R ₂₅ exp [B _{25/100} (1/T ₂ - 1/(298,15 K))]	-	3433	-	K

MOSFET Electrical characteristics (T_j=25°C unless otherwise specified, chip)

Symbol	Item	Condition		Value			Unit
				Min.	Typ.	Max	
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =400μA		1200	-	-	V
I _{DSS}	Zero gate voltage drain Current	V _{DS} =1200V, V _{GS} =0V, T _j =25°C		-	5	250	μA
V _{GS(th)}	Gate-source threshold Voltage	I _D =54mA, V _{DS} =V _{GS}	T _j =25°C	1.8	2.8	3.7	V
			T _j =150°C	-	2.1	-	
			T _j =175°C	-	2.0	-	
I _{GSS+}	Gate-Source Leakage Current	V _{GS} =15V, V _{DS} =0V	T _j =25°C	-	4	800	nA
I _{GSS-}		V _{GS} =-4V, V _{DS} =0V	T _j =25°C	-800	-4	-	
R _{DS(on)} (Chip)	Static drain-source On-state resistance	I _D =160A V _{GS} =15V	T _j =25°C	-	7.5	-	mΩ
			T _j =150°C	-	10.8	-	
			T _j =175°C	-	11.8	-	
V _{DS(on)} (Chip)	Static drain-source On-state Voltage	I _D =160A V _{GS} =15V	T _j =25°C	-	1.20	-	V
			T _j =150°C	-	1.73	-	
			T _j =175°C	-	1.89	-	
C _{iss}	Input Capacitance	V _D =1000V, V _{GS} =0V, f =100kHz		-	12.28	-	nF
C _{oss}	Output Capacitance			-	0.52	-	nF
C _{rss}	Reverse transfer Capacitance			-	0.04	-	nF
Q _g	Total gate charge	V _{DD} =800V, I _D =160A, V _{GS} =-4/+15V		-	536	-	nC
Q _{GS}	Gate-source charge			-	168	-	
Q _{GD}	Gate-drain charge			-	212	-	
R _{Gint}	Internal Gate Resistance	f =1MHz		-	1.5	-	Ω
t _{d(on)}	Turn-on delay time	V _{DD} =600V I _D =160A V _{GS} =-4/+15V R _{G(on)} =2.2Ω R _{G(off)} =2.2Ω Inductive load switching operation	T _j =25°C	-	22	-	ns
			T _j =150°C	-	18	-	
t _r	Rise time		T _j =25°C	-	26	-	ns
			T _j =150°C	-	23	-	
t _{d(off)}	Turn-off delay time		T _j =25°C	-	38	-	ns
			T _j =150°C	-	42	-	
t _f	Fall time		T _j =25°C	-	13	-	ns
			T _j =150°C	-	11	-	
E _{on}	Turn-on power dissipation		T _j =25°C	-	4.92	-	mJ
			T _j =150°C	-	5.81	-	
E _{off}	Turn-off power dissipation		T _j =25°C	-	0.82	-	mJ
			T _j =150°C	-	0.63	-	
R _{th(j-c)}	FET Thermal Resistance	Junction to Case		-	0.13	-	K/W
R _{th(c-f)}	Contact thermal Resistance	With thermal conductive grease, Note3		-	0.12	-	K/W

Note3: Assumes Thermal Conductivity of grease is 2.8 W/m · K and thickness is 50μm.

Body Diode Electrical characteristics (T_j=25°C unless otherwise specified, chip: Target)

Symbol	Item	Condition	Value			Unit
			Min.	Typ.	Max	
V _{SD}	Body Diode Forward Voltage	V _{GS} = -4V I _{SD} = 160A	T _j = 25°C	-	4.3	-
			T _j = 150°C	-	3.9	-
			T _j = 175°C	-	3.8	-
T _{rr}	Reverse recovery time	V _{DD} = 600V I _D = 160A	T _j = 25°C	-	28	-
			T _j = 150°C	-	31	-
Q _{rr}	Reverse recovery charge	V _{GS} = -4/+15V R _{GON} = R _{GOFF} = 2.2Ω	T _j = 25°C	-	2.14	-
			T _j = 150°C	-	4.19	-
E _{rr}	Diode switching power dissipation	Inductive load switching operation	T _j = 25°C	-	1.01	-
			T _j = 150°C	-	1.84	-

Test Conditions

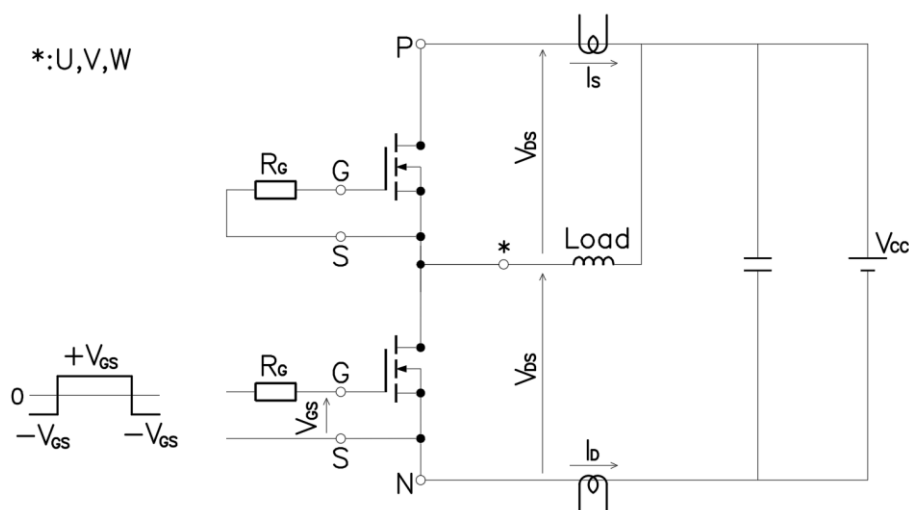


Figure 3. Switching time measure circuit

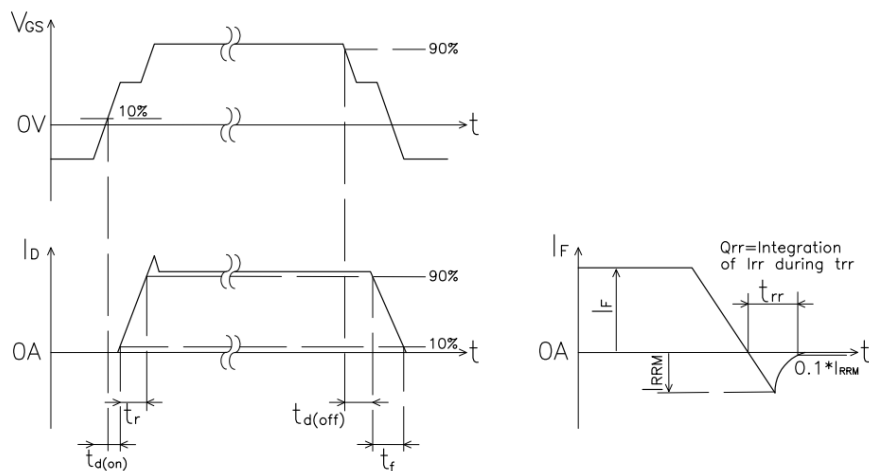


Figure 4. Switching time definition

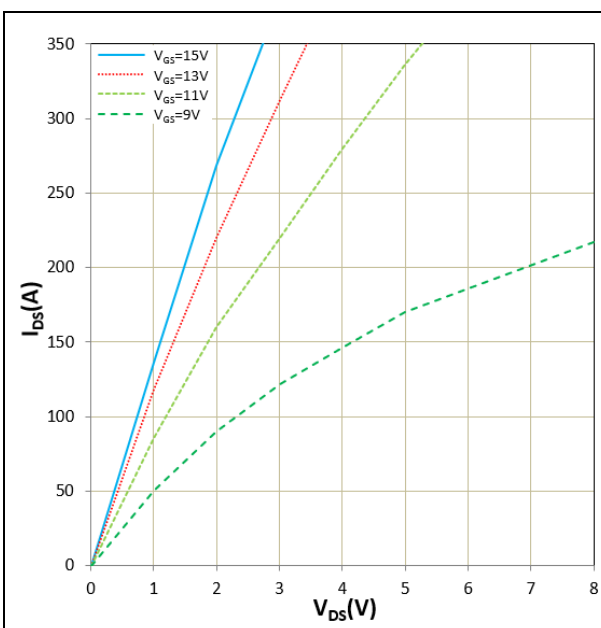


Figure 5. I_{DS} vs V_{DS}
 $T_j = 25^\circ\text{C}$

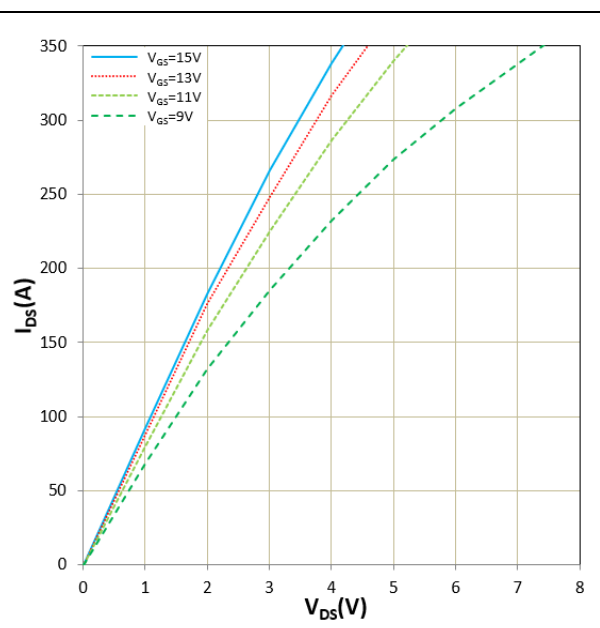


Figure 6. I_{DS} vs V_{DS}
 $T_j = 175^\circ\text{C}$

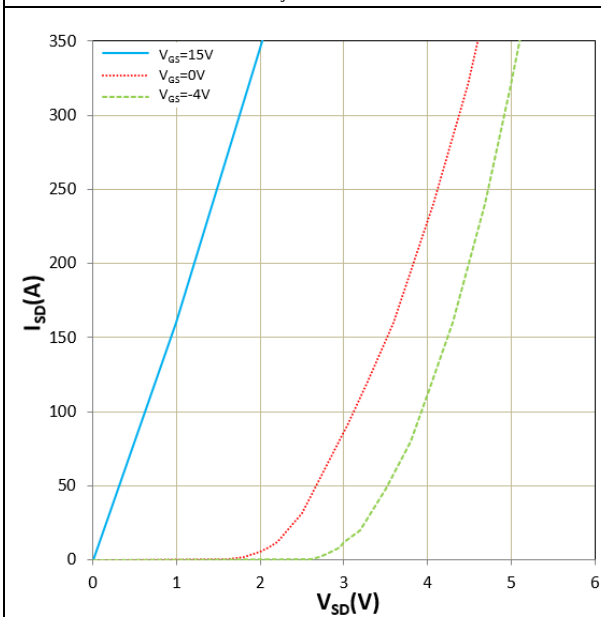


Figure 7. I_{SD} vs V_{SD}
 $T_j = 25^\circ\text{C}$

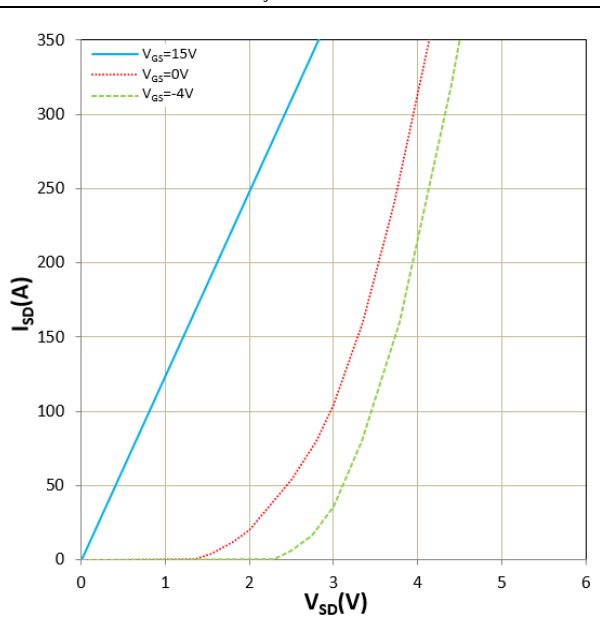


Figure 8. I_{SD} vs V_{SD}
 $T_j = 175^\circ\text{C}$

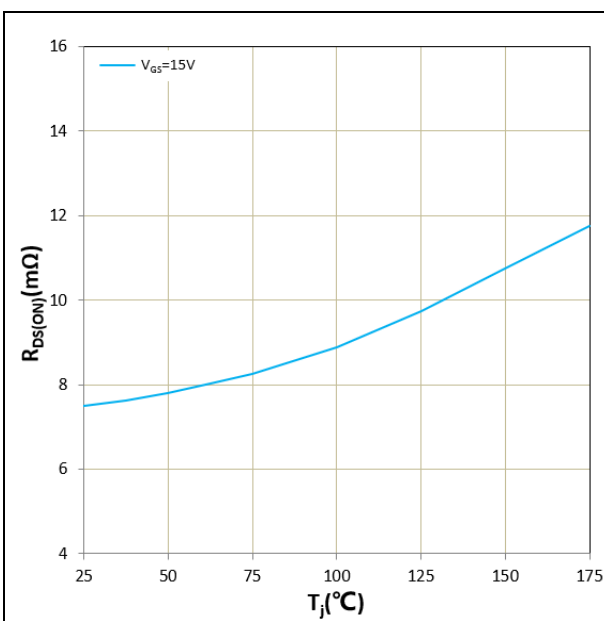


Figure 9. $R_{DS(ON)}$ vs T_j
 $I_D = 160A$

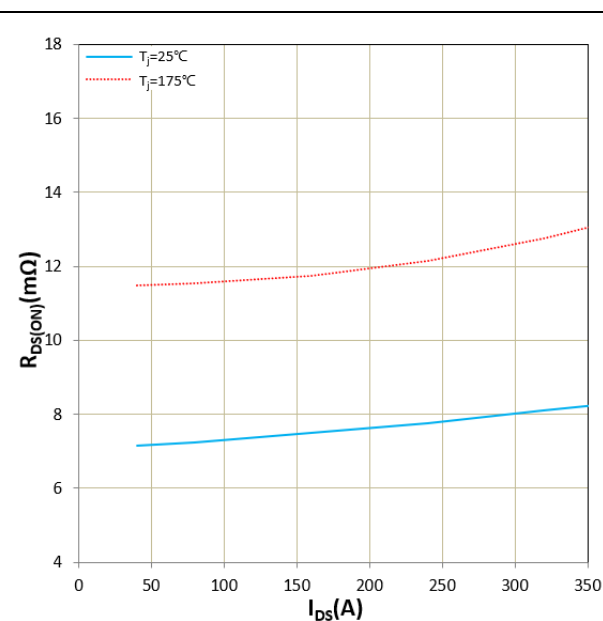


Figure 10. $R_{DS(ON)}$ vs I_{DS}
 $V_{GS} = +15V$

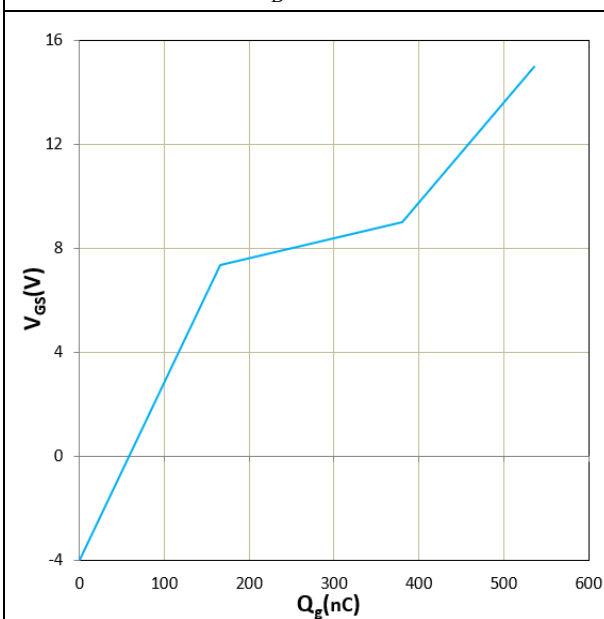


Figure 11. V_{GS} vs Q_g
 $T_j = 25^\circ C$, $V_{DS} = 800V$, $I_D = 160A$

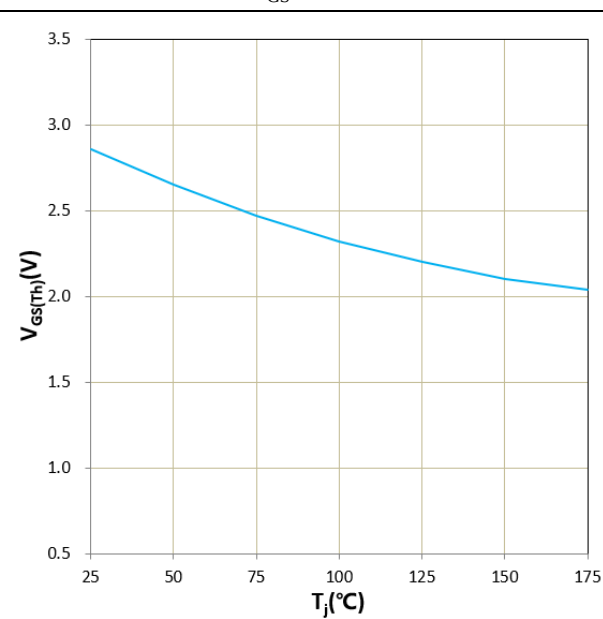


Figure 12. $V_{GS(TH)}$ vs T_j
 $V_{GS} = V_{DS}$, $I_D = 54mA$

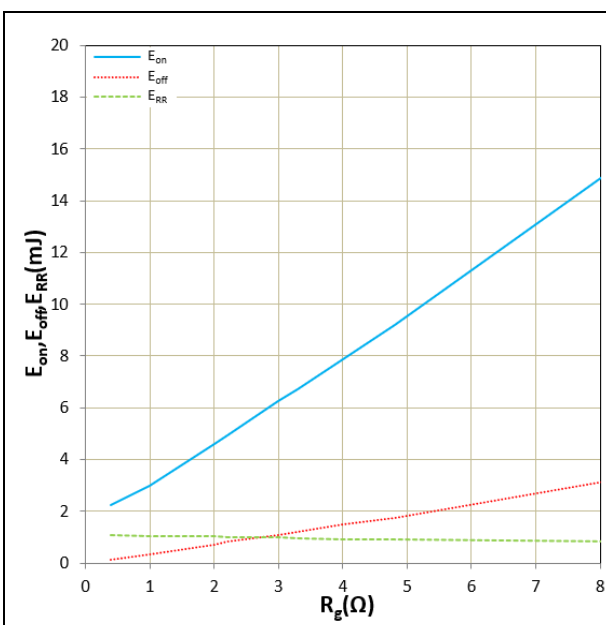


Figure 13. E_{on} , E_{off} , E_{rr} vs R_g
 $T_j = 25^\circ\text{C}$, $V_{DD} = 600\text{V}$, $I_D = 160\text{A}$, $V_{GS} = -4\text{V}/+15\text{V}$
 Inductive Load

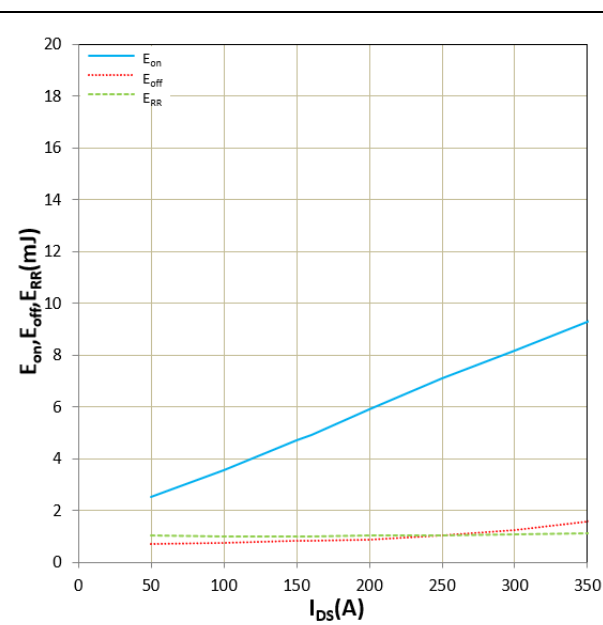


Figure 14. E_{on} , E_{off} , E_{rr} vs I_{DS}
 $T_j = 25^\circ\text{C}$, $V_{DD} = 600\text{V}$, $R_g = 2.2\Omega$, $V_{GS} = -4\text{V}/+15\text{V}$
 Inductive Load

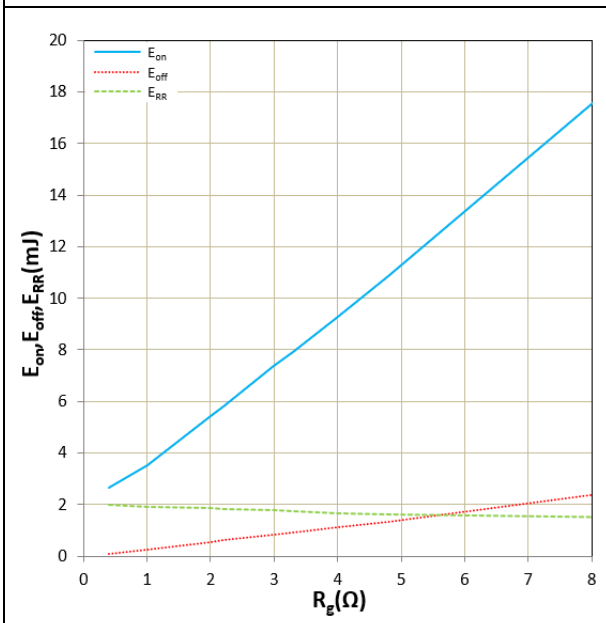


Figure 15. E_{on} , E_{off} , E_{rr} vs R_g
 $T_j = 150^\circ\text{C}$, $V_{DD} = 600\text{V}$, $I_D = 160\text{A}$, $V_{GS} = -4\text{V}/+15\text{V}$
 Inductive Load

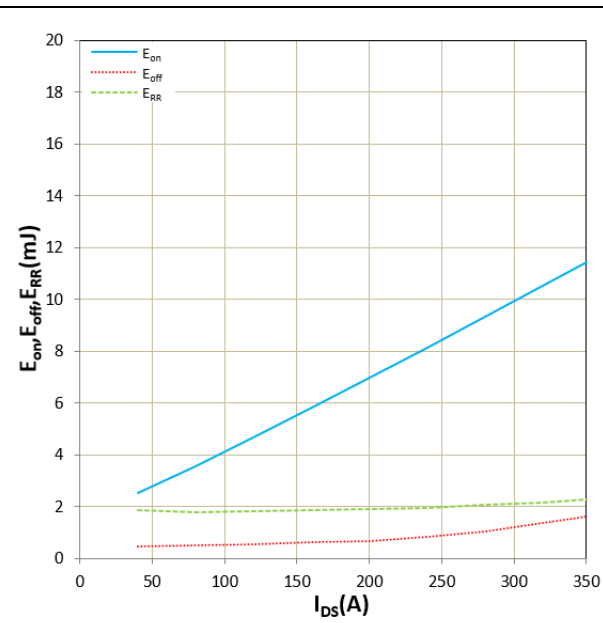
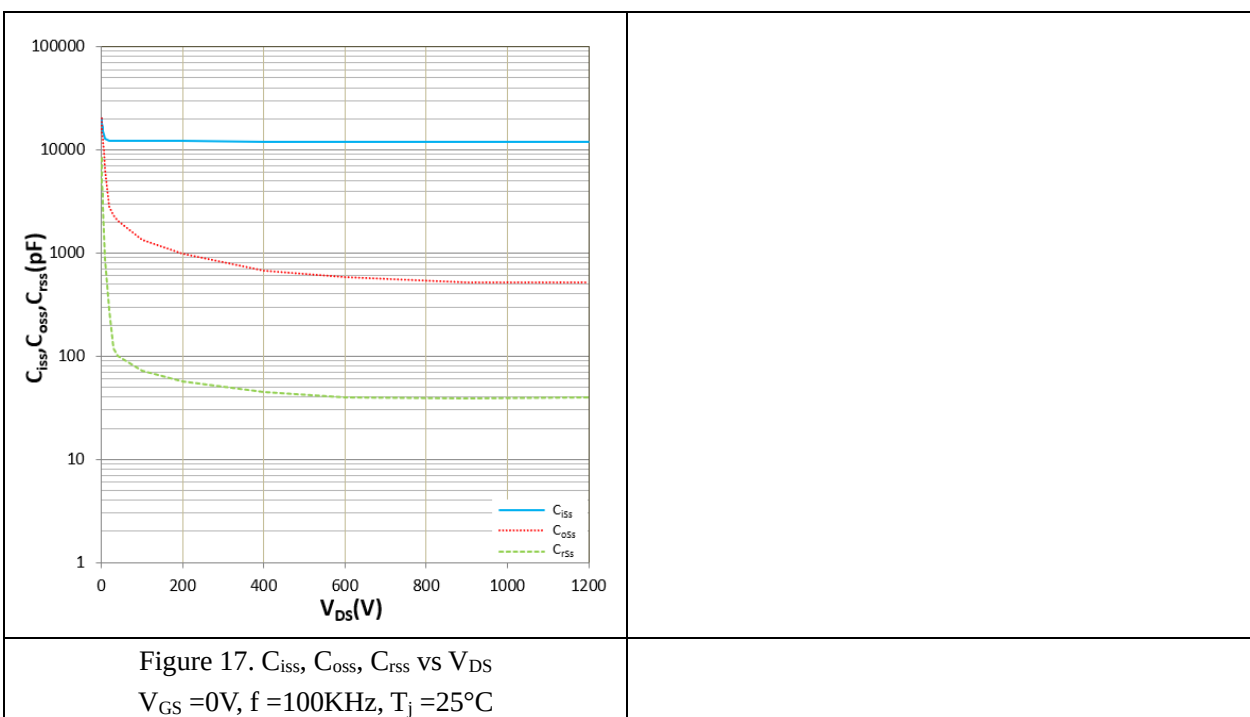
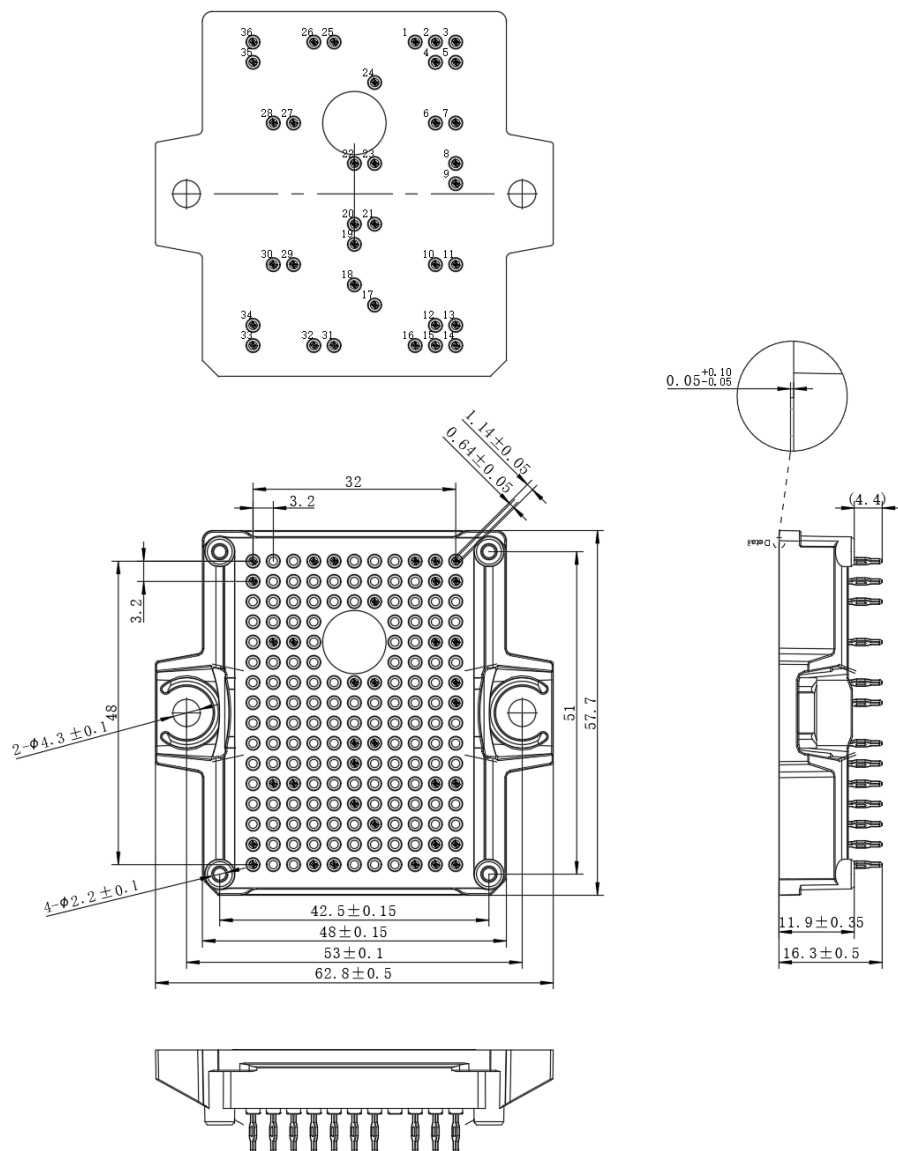


Figure 16. E_{on} , E_{off} , E_{rr} vs I_{DS}
 $T_j = 150^\circ\text{C}$, $V_{DD} = 600\text{V}$, $R_g = 2.2\Omega$, $V_{GS} = -4\text{V}/+15\text{V}$
 Inductive Load



Package dimensions



Pin	X	Y
1	25,6	48
2	28,8	48
3	32	48
4	28,8	44,8
5	32	44,8
6	28,8	35,2
7	32	35,2
8	32	28,8
9	32	25,6
10	28,8	12,8
11	32	12,8
12	28,8	3,2
13	32	3,2
14	32	0
15	28,8	0
16	25,6	0
17	19,2	6,4
18	16	9,6
19	16	16
20	16	19,2
21	19,2	19,2
22	16	28,8
23	19,2	28,8
24	19,2	41,6
25	12,8	48
26	9,6	48
27	6,4	35,2
28	3,2	35,2
29	6,4	12,8
30	3,2	12,8
31	12,8	0
32	9,6	0
33	0	0
34	0	3,2
35	0	44,8
36	0	48

Unit: mm

IMPORTANT NOTICE

This product data sheet describes the characteristics of this product for which a warranty is granted. Any such warranty is granted exclusively under the terms and conditions of the supply agreement. There will be no guarantee or of any kind for the product and its characteristics.

The data contained in this document is exclusively intended for technically trained staff. You and your technical departments will have to evaluate the product's suitability for the intended application and the completeness of the product data concerning such application.

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